



Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya  
Department of Electronics and Communication Engineering

TIME TABLE FOR 2017-18, EVEN SEMESTER

Year: I - A section

Room No:

DAY/ HOUR	1	2	3	4	5	6	7	8
	9.10 - 10.00	10.00 - 10.50	10.50 - 11.40	11.50 - 12.40	12.40 - 1.30	2.20 - 3.10	3.10 - 4.00	4.00- 4.40
MONDAY	ENGLISH	MATHS	BCME/CIVIL	CIRCUIT THEORY	EVS	CT/ WS LAB		
TUESDAY	MATHS	CIRCUIT THEORY	CHEMISTRY	BCME/MECH	EVS	ENGLISH	CHEMISTRY LAB	
WEDNESDAY	CHEMISTRY	EVS	ENGLISH	MATHS	Seminar	CIRCUIT THEORY	CHEMISTRY	Library
THURSDAY	CIRCUIT THEORY	MATHS	EVS	CHEMISTRY	SANSKRIT	ENGLISH	BCME/MECH	SEMI/IMM
FRIDAY	BCME/CIVIL	CHEMISTRY	CIRCUIT THEORY	MATHS	ENGLISH	CT/ WS LAB		

SL.NO	SUBJECT CODE	SUBJECT NAME	STAFF NAME
1	EN2T1	English II	Ms.G.Shanmugapriya
2	MA2T2	Basic Mathematics for Engineering II	Mr.V.K.Radhakrishnan
3	CH2T3	Engineering Chemistry	Mr. B. Muthukumaran
4	ME2T4	Basic Civil and Mechanical Engineering	Ms.Vithya/ Mr.V.john sundhar raj
5	EE2T5	Electric Circuit Theory	Mrs.M.A.Archana
6	CH2T6	Environmental Science and Engineering	Mr.V. Santhanam
7	SA2T2	Sanskrit & Indian Culture -II	Mr.N.Sridhar
8	CH2P7	Chemistry Lab	Mr. B. Muthukumaran
9	EE2P8	Circuit Theory Lab	Mrs.M.A.Archana
10	ME2P9	Basic Mechanical Workshop	Mr.Mohan
11	*	Seminar/Innovative/Motivational Class (SEMI/IMM)	Mrs.M.A.Archana

Class In charge

HOD/ECE



Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya  
Department of Electronics and Communication Engineering

TIME TABLE FOR 2017-18, EVEN SEMESTER

Year: I - B section

Room No:

DAY/ HOUR	1	2	3	4	5	6	7	8
	9.10 - 10.00	10.00 - 10.50	10.50 - 11.40	11.50 - 12.40	12.40 - 1.30	2.20 - 3.10	3.10 - 4.00	4.00 - 4.40
<b>MONDAY</b>	BCME/CIVIL	MATHS	CT/ WS LAB			BCME/MECH	ENGLISH	EVS
<b>TUESDAY</b>	EVS	MATHS	BCME/MECH	CHEMISTRY	CIRCUIT THEORY	MATHS	CHEMISTRY	LIBRARY
<b>WEDNESDAY</b>	CIRCUIT THEORY	CHEMISTRY	MATHS	ENGLISH	SEMI	SANSKRIT	CHEMISTRY LAB	
<b>THURSDAY</b>	ENGLISH	EVS	CT/ WS LAB			CIRCUIT THEORY	ENGLISH	CHEMISTRY
<b>FRIDAY</b>	CHEMISTRY	MATHS	CIRCUIT THEORY	ENGLISH	BCME/CIVIL	CIRCUIT THEORY	EVS	SEMI/IMM

SL.NO	SUBJECT CODE	SUBJECT NAME	STAFF NAME
1	EN2T1	English II	Mrs.R.Revathi
2	MA2T2	Basic Mathematics for Engineering II	Dr. A. Dhanalakshmi
3	CH2T3	Engineering Chemistry	Mrs. V. Vaidehi
4	ME2T4	Basic Civil and Mechanical Engineering	Mr.ChengaReddy/ Mrs.P.Punitha
5	EE2T5	Electric Circuit Theory	Mr.A.Niranjana
6	CH2T6	Environmental Science and Engineering	Dr.R. Prbakarakrishnan
7	SA2T2	Sanskrit & Indian Culture -II	Mr.S.Thiyagarajan
8	CH2P7	Chemistry Lab	Dr.R. Prbakarakrishnan
9	EE2P8	Circuit Theory Lab	Mr.A.Niranjana
10	ME2P9	Basic Mechanical Workshop	Mr.Vetrivel
11	*	Seminar/Innovative/Motivational Class(SEMI/IMM)	Mr.A.Niranjana

Class In charge

HOD/ECE



Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya  
Department of Electronics and Communication Engineering

TIME TABLE FOR 2017-18, EVEN SEMESTER

Year: I - C section

Room No:

DAY/ HOUR	1	2	3	4	5	6	7	8
	9.10 - 10.00	10.00 - 10.50	10.50 - 11.40	11.50 -12.40	12.40 - 1.30	2.20 - 3.10	3.10 - 4.00	4.00- 4.40
<b>MONDAY</b>	ENGLISH	BCME/MECH	MATHS	CIRCUIT THEORY	BCME/CIVIL	CHEMISTRY	EVS	LIBRARY
<b>TUESDAY</b>	MATHS	ENGLISH	EVS	BCME/CIVIL	CHEMISTRY	CT/ WS LAB		
<b>WEDNESDAY</b>	CIRCUIT THEORY	BCME/MECH	MATHS	EVS	Seminar	CHEMISTRY	CIRCUIT THEORY	SEMI/IMM
<b>THURSDAY</b>	MATHS	CIRCUIT THEORY	ENGLISH	MATHS	SANSKRIT	CT/ WS LAB		
<b>FRIDAY</b>	EVS	CHEMISTRY	CIRCUIT THEORY	ENGLISH	ENGLISH	CHEMISTRY	CHEMISTRY LAB	

SL.NO	SUBJECT CODE	SUBJECT NAME	STAFF NAME
1	EN2T1	English II	Mr.K.Narasimabharathi
2	MA2T2	Basic Mathematics for Engineering II	Mrs.K.Bharathi
3	CH2T3	Engineering Chemistry	Mrs. Ushranipisipaty
4	ME2T4	Basic Civil and Mechanical Engineering	Mr.S. D. Sathish Kumar/Mr.M.Dinesh
5	EE2T5	Electric Circuit Theory	Mr.T.Dinesh Kumar
6	CH2T6	Environmental Science and Engineering	Dr.N.Prabu
7	SA2T2	Sanskrit & Indian Culture –II	Mr.S.Thiyagarajan
8	CH2P7	Chemistry Lab	Mrs. Ushranipisipaty
9	EE2P8	Circuit Theory Lab	Mr.T.Dinesh Kumar
10	ME2P9	Basic Mechanical Workshop	Mr.S. D. Sathish Kumar
11	*	Seminar/Innovative/Motivational Class(SEMI/IMM)	Mr.T.Dinesh Kumar

Class In charge

HOD/ECE



Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya  
Department of Electronics and Communication Engineering

TIME TABLE FOR 2017-18, EVEN SEMESTER

Year: I - D section

Room No:

DAY/ HOUR	1	2	3	4	5	6	7	8
	9.10 - 10.00	10.00 - 10.50	10.50 - 11.40	11.50 - 12.40	12.40 - 1.30	2.20 - 3.10	3.10 - 4.00	4.00- 4.40
<b>MONDAY</b>	ENGLISH	MATHS	CHEMISTRY	EVS	SANSKRIT	CHEMISTRY	BCME/MECH	CIRCUIT THEORY
<b>TUESDAY</b>	CIRCUIT THEORY	CHEMISTRY	ENGLISH	MATHS	Seminar	EVS	CIRCUIT THEORY	LIBRARY
<b>WEDNESDAY</b>	BCME/CIVIL	ENGLISH	MATHS	CIRCUIT THEORY	BCME/CIVIL	CT/ WS LAB		
<b>THURSDAY</b>	CHEMISTRY	MATHS	EVS	BCME/MECH	ENGLISH	CIRCUIT THEORY	CHEMISTRY LAB	
<b>FRIDAY</b>	MATHS	ENGLISH	CT/ WS LAB			CHEMISTRY	EVS	SEMI/IMM

SL.NO	SUBJECT CODE	SUBJECT NAME	STAFF NAME
1	EN2T1	English II	Mr.P.S.Govindharajan
2	MA2T2	Basic Mathematics for Engineering II	Dr.T.N.Kavitha
3	CH2T3	Engineering Chemistry	Mrs.K.Indhumathy
4	ME2T4	Basic Civil and Mechanical Engineering	Mr.Venkatakoteswararao/ Mr.M.Sekar
5	EE2T5	Electric Circuit Theory	Mr.B.Chandrasekaran
6	CH2T6	Environmental Science and Engineering	Dr.B.Narayanamoorthy
7	SA2T2	Sanskrit & Indian Culture -II	Mr.S.Thiyagarajan
8	CH2P7	Chemistry Lab	Mrs.K.Indhumathy
9	EE2P8	Circuit Theory Lab	Mr.B.Chandrasekaran / Mrs.G.Poornima
10	ME2P9	Basic Mechanical Workshop	Mr.Arumugam
11	*	Seminar/Innovative/Motivational Class(SEMI/IMM)	Mr.B.Chandrasekaran

Class In charge

HOD/ECE



Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya  
Department of Electronics and Communication Engineering

TIME TABLE FOR 2017-18, EVEN SEMESTER

Year: II - A section

Room No:

DAY/ HOUR	1	2	3	4	5	6	7	8
	9.10 - 10.00	10.00 - 10.50	10.50 - 11.40	11.50 - 12.40	12.40 - 1.30	2.20 - 3.10	3.10 - 4.00	4.00- 4.40
MONDAY	MATHS	TLW	AC /OOPS Lab			AIC	CS	SEMI/IMM
TUESDAY	AIC	OOPS	AC	TLW	CS	AC /AIC Lab		
WEDNESDAY	OOPS	TLW	AIC /OOPS Lab			CS	MATHS	LIBRARY
THURSDAY	CS	MATHS	OOPS	AC	TLW	MATHS	AIC	OOPS
FRIDAY	AC	AIC	MATHS	CS	OOPS	TLW	AC	SANSKRIT

SL.NO	SUBJECT CODE	SUBJECT NAME	STAFF NAME
1	MC4T1	Applied Mathematics for Electronic Engineers – II	Mr.K.Saravanan
2	EC4T3	Analog Integrated Circuits	Mrs.V.Jayapradha
3	EC4T4	Analog Communication	Mr.S.Vijayaraghavan
4	EC4T7	Control System Engineering	Mrs.K.Anitha
5	EC4T9	Transmission Lines and Wave Guides	Mrs.G.Poornima
6	CS4T6	Object Programming using C++	Mrs.M.saraswathi
7	SA4T4	Sanskrit & Indian Culture -IV	
8	EC4P8	Analog Integrated circuits Laboratory	Mrs.V.Jayapradha/ Mrs.K.Anitha
9	EC4P5	Analog Communication Laboratory	Mr.S.Vijayaraghavan
10	CS4P9	Object Oriented Programming Laboratory using C++	Mrs.M.saraswathi
11	SS4P2	Soft Skill– II	
12	PT4P1	Industrial Training and Practice - I	Mrs.V.Jayapradha
13	*	Seminar/Innovative/Motivational Class(SEMI/IMM)	Mrs.V.Jayapradha

Class In charge

HOD/ECE



Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya  
Department of Electronics and Communication Engineering

TIME TABLE FOR 2017-18, EVEN SEMESTER

Year: II - B section

Room No:

DAY/ HOUR	1	2	3	4	5	6	7	8
	9.10 - 10.00	10.00 - 10.50	10.50 - 11.40	11.50 - 12.40	12.40 - 1.30	2.20 - 3.10	3.10 - 4.00	4.00- 4.40
MONDAY	OOPS	MATHS	AC	AIC	MATHS	TLW	AC	CS
TUESDAY	MATHS	TLW	AC/OOPS Lab			CS	OOPS	SEMI/IMM
WEDNESDAY	CS	AC	AIC	TLW	OOPS	MATHS	TLW	SANSKRIT
THURSDAY	TLW	AC	AIC	OOPS	CS	AC/AIC Lab		
FRIDAY	AIC	MATHS	AIC/OOPS Lab			CS	Library	OOPS

SL.NO	SUBJECT CODE	SUBJECT NAME	STAFF NAME
1	MC4T1	Applied Mathematics for Electronic Engineers – II	Ms.R.Kalaiarasi
2	EC4T3	Analog Integrated Circuits	Mr.A.Rajasekaran
3	EC4T4	Analog Communication	Mr.D.Muthukumaran
4	EC4T7	Control System Engineering	Mrs.V.Uma
5	EC4T9	Transmission Lines and Wave Guides	Mr.A.Niranjan
6	CS4T6	Object Programming using C++	Mrs.M.Gayathri
7	SA4T4	Sanskrit & Indian Culture -IV	
8	EC4P8	Analog Integrated circuits Laboratory	Mr.A.Rajasekaran
9	EC4P5	Analog Communication Laboratory	Mr.D.Muthukumaran
10	CS4P9	Object Oriented Programming Laboratory using C++	Mrs.M.Gayathri
11	SS4P2	Soft Skill– II	
12	PT4P1	Industrial Training and Practice - I	Mr.D.Muthukumaran
13	*	Seminar/Innovative/Motivational Class(SEMI/IMM)	Mr.D.Muthukumaran

Class In charge

HOD/ECE



Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya  
Department of Electronics and Communication Engineering

TIME TABLE FOR 2017-18, EVEN SEMESTER

Year: II - C section

Room No:

DAY/ HOUR	1	2	3	4	5	6	7	8
	9.10 - 10.00	10.00 - 10.50	10.50 - 11.40	11.50 - 12.40	12.40 - 1.30	2.20 - 3.10	3.10 - 4.00	4.00- 4.40
MONDAY	TLW	CS	AC	AIC	CS	MATHS	OOPS	SEMI/IMM
TUESDAY	OOPS	TLW	CS	MATHS	OOPS	AC	SANSKRIT	TLW
WEDNESDAY	CS	AC	AIC	TLW	MATHS	AIC /OOPS Lab		
THURSDAY	AIC	CS	AC /OOPS Lab			OOPS	MATHS	Library
FRIDAY	TLW	MATHS	OOPS	AC	AIC	AC /AIC Lab		

SL.NO	SUBJECT CODE	SUBJECT NAME	STAFF NAME
1	MC4T1	Applied Mathematics for Electronic Engineers – II	Mr. P. Balaji
2	EC4T3	Analog Integrated Circuits	Mrs.R.Jayalakshmi
3	EC4T4	Analog Communication	Dr.K.Umapathy
4	EC4T7	Control System Engineering	Mr.B.Chandrasekaran
5	EC4T9	Transmission Lines and Wave Guides	Mr.L.Sathish Kumar
6	CS4T6	Object Programming using C++	Mrs.D.Gayathri
7	SA4T4	Sanskrit & Indian Culture -IV	
8	EC4P8	Analog Integrated circuits Laboratory	Mrs.R.Jayalakshmi
9	EC4P5	Analog Communication Laboratory	Mr.L.Sathish Kumar
10	CS4P9	Object Oriented Programming Laboratory using C++	Mrs.D.Gayathri
11	SS4P2	Soft Skill– II	
12	PT4P1	Industrial Training and Practice - I	Mr.L.Sathish Kumar
13	*	Seminar/Innovative/Motivational Class(SEMI/IMM)	Mr.L.SathishKumarS

Class In charge

HOD/ECE



Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya  
Department of Electronics and Communication Engineering

TIME TABLE FOR 2017-18, EVEN SEMESTER

Year: III

Room No:

DAY/ HOUR	1	2	3	4	5	6	7	8
	9.10 - 10.00	10.00 - 10.50	10.50 - 11.40	11.50 - 12.40	12.40 - 1.30	2.20 - 3.10	3.10 - 4.00	4.00- 4.40
MONDAY	ES	OC	ES/ VLSI LAB			VLSI	ES	DCN
TUESDAY	RP	MW	ISD/ VLSI LAB			RP	VLSI	Aptitude Skills
WEDNESDAY	OC	MW	VLSI	RP	ES	DCN	VLSI	OC
THURSDAY	DCN	RP	ISD/ES LAB			DCN	OC	MW
FRIDAY	MW	VLSI	DCN	RP	OC	ES	MW	Sanskrit

SL.NO	SUBJECT CODE	SUBJECT NAME	STAFF NAME
1	MC6T1	Applied Random Processes	Mr.P.Nagarajan
2	EC6T2	Embedded System Design	Mr.M.Vinoth
3	EC6T4	VLSI Design	Dr.G.Senthilkumar
4	EC6T5	Microwave Engineering	Mrs.K.Anitha
5	EC6T6	Optical Communications	Mr.S.Vijayaraghavan
6	EC6T8	Data Communication Networks	Dr.S.Omkumar
7	EC6P6	Embedded system Design Laboratory	Mr.M.Vinoth
8	EC6P8	Innovative System Design Laboratory	Dr.P.Venkatesan / Mr.S.Selvakumar
9	EC6P9	VLSI Design Laboratory	Dr.G.Senthilkumar/ Mrs.V.Uma
10	AS6P2	Aptitude Skills - II	Dr.Sivakumar
11	PT6P2	Industrial Training and Practice - II	Dr.S.Omkumar

Class In charge

HOD/ECE





Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya  
Department of Electronics and Communication Engineering

TIME TABLE FOR 2017-18, EVEN SEMESTER

Year: IV A

Room No:

DAY/ HOUR	1	2	3	4	5	6	7	8
	9.10 - 10.00	10.00 - 10.50	10.50 - 11.40	11.50 - 12.40	12.40 - 1.30	2.20 - 3.10	3.10 - 4.00	4.00- 4.40
<b>MONDAY</b>	NEURAL	NEURAL	E-III- AMC/ DIP(III A CLASS ROOM)			SAT	E-IV - CC /AWC(I A CLASS ROOM)	
<b>TUESDAY</b>	SAT	SAT	E-IV - CC /AWC(II B CLASS ROOM)		NEURAL	Seminar / Library		
<b>WEDNESDAY</b>	NEURAL	NEURAL	E-IV - CC /AWC(II A CLASS ROOM)		SAT	E-III- AMC/ DIP (II C CLASS ROOM)		
<b>THURSDAY</b>	SAT	SAT	NEURAL	NEURAL		E-III- AMC/ DIP(I C CLASS ROOM)		
<b>FRIDAY</b>	SAT	SAT	E-III- AMC/ DIP (I D CLASS ROOM)			NEURAL	E-IV - CC /AWC(II C CLASS ROOM)	

SL.NO	SUBJECT CODE	SUBJECT NAME	STAFF NAME
1	EC8T2	Satellite Communication	Mrs.G.Poornima
2	EC8T3	Neural networks and Fuzzy Logic	Mrs.V.Jayapradha
3	EC8E2	Elective -III - Digital Image Processing	Dr.P.Venkatesan
4	EC8E4	Elective -III - Advanced Microcontroller	Dr.J.Vaideeswaran
5	EC8E7	Elective -IV - Advanced Wireless Communication	Mr.T.Dinesh Kumar
6	CS8E8	Elective -IV - Computer Communication	Mr. S.Selvakumar

Class In charge

HOD/ECE



Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya  
Department of Electronics and Communication Engineering

TIME TABLE FOR 2017-18, EVEN SEMESTER

Year: IV B

Room No:

DAY/ HOUR	1	2	3	4	5	6	7	8
	9.10 - 10.00	10.00 - 10.50	10.50 - 11.40	11.50 - 12.40	12.40 - 1.30	2.20 - 3.10	3.10 - 4.00	4.00- 4.40
<b>MONDAY</b>	SAT	SAT	E-III- AMC/DIP			Lib	E-IV – CC/AWC	
<b>TUESDAY</b>	NEURAL	NEURAL	E-IV – CC/AWC			SAT	SAT	NEURAL
<b>WEDNESDAY</b>	SAT	LIB	E-IV – CC /AWC		NEURAL	E-III- AMC/DIP		NEURAL
<b>THURSDAY</b>	SAT	SAT	Lib	NEURAL		E-III- AMC /DIP		Lib
<b>FRIDAY</b>	SAT	NEURAL	E-III- AMC /DIP			NEURAL	E-IV – CC /AWC	

SL.NO	SUBJECT CODE	SUBJECT NAME	STAFF NAME
1	EC8T2	Satellite Communication	Mr. S.Chandramohan
2	EC8T3	Neural networks and Fuzzy Logic	Mr.D.MuthuKumaran
3	EC8E2	Elective -III - Digital Image Processing	Dr.P.Venkatesan
4	EC8E4	Elective -III - Advanced Microcontroller	Mr.L.Sathish Kumar
5	EC8E7	Elective -IV - Advanced Wireless Communication	Mr.T.Dinesh Kumar
6	CS8E8	Elective -IV - Computer Communication	Mr.J.Vinoth Kumar

Class In charge

HOD/ECE



Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya  
Department of Electronics and Communication Engineering

TIME TABLE FOR 2017-18, EVEN SEMESTER

Year: I – M.E EMBEDDED SYSTEM

Room No:

DAY/ HOUR	1	2	3	4	5	6	7	8
	9.10 - 10.00	10.00 - 10.50	10.50 - 11.40	11.50 - 12.40	12.40 - 1.30	2.20 - 3.10	3.10 - 4.00	4.00- 4.40
MONDAY	RTOS	ECSD	ECSD	STES	EN	DIP	DIP	
TUESDAY	VLSI	RTOS	RTOS	LIB	SEMI	ECSD	ECSD	
WEDNESDAY	STES	ECSD	ES Lab			RTOS	RTOS	
THURSDAY	EN	EN	VLSI	VLSI	STES	DIP	STES	
FRIDAY	DIP	DIP	EN	EN	STES	VLSI	VLSI	

SL.NO	SUBJECT CODE	SUBJECT NAME	STAFF NAME
1		Real Time Operating System	Mr.A.Rajasekaran
2		Software Technology for Embedded Systems	Mr.J.Vinoth Kumar
3		Embedded Networking	Mr.S.Selvakumar
4		Embedded Communication and Software Design	Dr.S.Omkumar
5		ELECTIVE –II– DIP	Mr.R.Viswanath
6		ELECTIVE –III – VLSI architecture and Design Methodology	Dr.G.Senthilkumar
7		Embedded Systems Lab	Dr.K.Umapathy / Dr.S.Omkumar

Class In charge

HOD/ECE



Sri ChandrasekharendraSaraswathiViswaMahavidyalaya  
Department of Electronics and Communication Engineering

TIME TABLE FOR 2017-18, EVEN SEMESTER

Year: I – M.Tech (EDT)

Room No:

DAY/ HOUR	1	2	3	4	5	6	7	8
	9.10 - 10.00	10.00 - 10.50	10.50 - 11.40	11.50 - 12.40	12.40 - 1.30	2.20 - 3.10	3.10 - 4.00	4.00- 4.40
MONDAY	MEMS	DQR	SMI/LIB	SIM/LIB		ADSP	ADSP	
TUESDAY	ADSP	MSD	MSD	MEMS		DQR	DQR	IDEP
WEDNESDAY	DQR	DQR	IDEP	IDEP		MSD	SMI/LIB	
THURSDAY	IDEP	IDEP	MSD	MSD		MEMS	MEMS	IDEP
FRIDAY	Lib	Lib	MEMS	Lib		ADSP	ADSP	

SL.NO	SUBJECT CODE	SUBJECT NAME	STAFF NAME
1	EDTS2T1	Industrial Design of Electronic Products	Ms.V.Uma
2	EDTS2T2	MEMS and Applications	Ms.R.Jayalakshmi
3	EDTS2T3	Design for Quality and Reliability	Ms.A.Archana
4	EDTS2T4	Elective - ADSP	Dr.P.Venkatesan
5	EDTS2T5	Elective – Mixed signal design	Dr.K.Umapathy

Class In charge

HOD/ECE



Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya  
Department of Electronics and Communication Engineering

TIME TABLE FOR 2017-18, EVEN SEMESTER

Year: IV A

Room No:

DAY/ HOUR	1	2	3	4	5	6	7	8
	9.10 - 10.00	10.00 - 10.50	10.50 - 11.40	11.50 - 12.40	12.40 - 1.30	2.20 - 3.10	3.10 - 4.00	4.00- 4.40
<b>MONDAY</b>	NEURAL	SAT	E-III- AMC/ DIP(III A CLASS ROOM)			E-IV - CC /AWC(I A CLASS ROOM)		
<b>TUESDAY</b>	SAT	SAT	NEURAL	NEURAL				

SL.NO	SUBJECT CODE	SUBJECT NAME	STAFF NAME
1	EC8T2	Satellite Communication	Mrs.G.Poornima
2	EC8T3	Neural networks and Fuzzy Logic	Mrs.V.Jayapradha
3	EC8E2	Elective -III - Digital Image Processing	Dr.P.Venkatesan
4	EC8E4	Elective -III - Advanced Microcontroller	Dr.J.Vaideeswaran
5	EC8E7	Elective -IV - Advanced Wireless Communication	Mr.T.Dinesh Kumar
6	CS8E8	Elective -IV - Computer Communication	Mr. S.Selvakumar

Class In charge

HOD/ECE



Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya  
Department of Electronics and Communication Engineering

TIME TABLE FOR 2017-18, EVEN SEMESTER

Year: IV B

Room No:

DAY/ HOUR	1	2	3	4	5	6	7	8
	9.10 - 10.00	10.00 - 10.50	10.50 - 11.40	11.50 - 12.40	12.40 - 1.30	2.20 - 3.10	3.10 - 4.00	4.00 - 4.40
MONDAY	SAT	NEURAL	E-III- AMC			E-IV - CC		
TUESDAY	NEURAL	NEURAL	SAT	SAT				

SL.NO	SUBJECT CODE	SUBJECT NAME	STAFF NAME
1	EC8T2	Satellite Communication	Mr. S.Chandramohan
2	EC8T3	Neural networks and Fuzzy Logic	Mr.D.MuthuKumaran
3	EC8E2	Elective -III - Digital Image Processing	Dr.P.Venkatesan
4	EC8E4	Elective -III - Advanced Microcontroller	Mr.L.Sathish Kumar
5	EC8E7	Elective -IV - Advanced Wireless Communication	Mr.T.Dinesh Kumar
6	CS8E8	Elective -IV - Computer Communication	Mr.J.Vinoth Kumar

Class In charge

HOD/ECE