



Sri Chandrasekharendra Saraswathi Viswa Maha Vidyalaya University
Department of Electronics and Communication Engineering

SCSVMV/ECE/IRD/IR/086

Date: 26.02.2015

Sub: Minutes of 16th Research Colloquium meeting organized in ECE department on 25/02/15
– Regarding

1. The Department of ECE organized the 16th “Research Colloquium” on 25/02/15, Wednesday in Mega Seminar Hall at 4.40 pm.

2. The following staff members were Present

Prof Dr.S.Sivanandam.
Shri R.Viswanath.
Shri A.Rajasekaran
Shri.S.Vijayaraghavan.
Shri M.Senthilkumar.
Shri.B.Chandrasekaran.
Shri. S.Selvakumar.
Shri.G.Poornima.
Shri. V.Jayapradha.
Shri. R.Kamalakannan.
Shri.M.Vinoth.
Shri S.Chandramohan.
Shri.M.A.Archana
Shri. V.Uma.
Shri. D.Muthukumaran
Shri.L.Sathish Kumar.
Shri.T.DineshKumar.
Shri A.Niranjana.
Shri. K.Anitha
Shri. M.Selvakumar .
Shri T.S.Raghavendran.
Shri.P.N.Vijai Amritraj
Shri.K.Parimala
Shri. K.M.Sivakumar
Shri.M.Bhuvaneshwari
Shri..E.Senbagam.
Shri.T.Bhuvaneshwari
Shri R.Palani.

3. The following members were absent

Dr K.Umapathy (On CL)
Dr P.Venkatesan
Dr S.Om Kumar.
ShriG.Senthilkumar
Dr.V.Balaji (On CL)

4. Research Scholar: Mrs. V.Jayapradha, Research Scholar, Part time, internal (ECE).
Assistant Professor/ECE, SCSVMV University, Kanchipuram.

Research Topic : VLSI Testing – Boundary Scan Testing and Test Reduction.

Synopsis : Topic of Research Colloquium is an overview of the VLSI testing and test reduction based on boundary scan. In this presentation, she discussed about the JTAG boundary scan. The presentation gave an overview of testing and compression technique for reducing test vector, test time and test power. She also discussed about scan based testing kit (JTAG IEEE 1149.1). The proposed model is much useful for improving the lifetime of the Processors.

5. File Photo of the Colloquium



6. Research Colloquium was organized by A.Rajasekaran, Assistant Professor, ECE department.

A.Rajasekaran
(Research Colloquium coordinator)

Dr.M.Sivanandam
(HOD/ECE)

Encl:

1. Attendance Sheet.

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