



SRI CHANDRASEKHARENDRASARASWATHI VISWA MAHAVIDYALAYA  
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

**RESEARCH COLLOQUIUM**

**REPORT:**

- ❖ 42<sup>nd</sup> Research colloquium organized by the Department of ECE as per the directions from Research and Publication wing of SCSVMV on **30.08.2019** at 12.30pm.
- ❖ Research Colloquium given by: **Mrs.R.Jayalakshmi**, Internal Research Scholar.
  - [Guide: Dr.M.Senthilkumaran, Expert: Dr.H.Ranganathan]
- ❖ **Research Topic:**  
“DESIGN OF COMBINATIONAL CIRCUITS IN REVERSIBLE RECONFIGURABLE LOGIC”

**FILE PHOTO:**

