



**Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya  
Department of Electronics and Communication  
Engineering  
&  
Centre for Curriculum Design and Development**

**Cordially Invites You for**

**Seminar on  
“CURRICULUM ENRICHMENT”**

For circuit branch faculty members  
(ECE, EEE, EIE)

**Date: 20/03/2017 (Monday) @ 10 A.M.**

**Venue: Mechanical Seminar Hall**

Resource Person : **Dr. Vijaya Rajeswar,**  
M.D, VI Microsystems,  
Chennai.