



**SRI CHANDRASEKHARENDRA SARASWATHI VISWA MAHAVIDHYALAYA**  
**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING**  
**ODD SEMESTER FOR THE ACADEMIC YEAR 2020-21 - LECTURE PLAN**

**II YEAR PART TIME M.E(POWER SYSTEMS)**

Sno	DAY	Session	
		6 pm to 7.30 pm	Topic to be covered
1	11/8/2020	Digital Protection of Power Systems	Introduction, block diagram of numerical relay
2	13/08/2020	FACTS	Reactive power control in electrical power transmission lines
3	18/8/2020	Digital Protection of Power Systems	sampling theorem, correlation with a reference wave,
4	20/08/2020	FACTS	Uncompensated transmission line - series compensation
5	25/08/2020	Digital Protection of Power Systems	least error squared (LES) technique, digital filtering
6	27/08/2020	FACTS	Basic concepts of static VAR Compensator (SVC) - Thyristor Switched Series capacitor (TCSC)

Subject Name	Staff Name
Digital Protection of Power Systems	Dr.M.Rathinakumar
FACTS	Mr.R.Karthikeyan