ABOUT THE CONFERENCE

The Conference is envisioned as a premier platform bringing together academia, industry, researchers, technology professionals and students to explore emerging developments and opportunities in the rapidly evolving semiconductor ecosystem. With a focus on semiconductor devices, advanced materials, power electronics, packaging technologies, testing, reliability and smart manufacturing, the Conference aims to promote meaningful knowledge exchange and collaboration across the semiconductor value chain. The programme also addresses critical future-oriented themes including Artificial Intelligence, sustainability in semiconductor manufacturing, supply-chain challenges, curriculum alignment and workforce development. A distinctive feature of the Conference is its Special Track, which explores interdisciplinary perspectives connecting AI and computational technologies, Indian Knowledge Systems, emerging technology education and sustainable semiconductor development. Through expert sessions, technical deliberations, industry interaction and specialised learning opportunities, the Conference seeks to create a vibrant forum for innovation, collaboration and capacity building, contributing towards the development of a strong and sustainable semiconductor ecosystem in India.

ABOUT IIT MADRAS

Indian Institute of Technology Madras (IIT Madras) is a premier institution of higher learning and research, playing a significant role in advancing science, technology, innovation and national development. Through its strong academic and research ecosystem, IIT Madras continues to contribute to cutting-edge technological advancements, industry collaboration and the development of future-ready talent. As the Organiser of ICAST 2026, IIT Madras brings together leading academicians, researchers, industry experts and institutions to create a meaningful platform for knowledge exchange and technological collaboration in the rapidly evolving semiconductor domain.

ABOUT SCSVMV

Sri Chandrasekharendra Saraswathi Viswa Mahavidyalaya (SCSVMV), Enathur, Kanchipuram, is a distinguished institution committed to excellence in education, research, innovation and interdisciplinary learning. Guided by the vision of harmoniously integrating India's rich knowledge traditions with contemporary scientific and technological education, the University continues to foster an environment that encourages intellectual exploration and innovation.In association with IIT Madras, SCSVMV is proud to contribute to ICAST 2026, supporting the Conference's vision of promoting academic excellence, research collaboration and capacity building in emerging semiconductor technologies. The involvement of the University's institutional leadership in the Advisory Committee further reflects SCSVMV's association with the Conference.

Advisory Committee

Chair (Advisory Committee)

Prof. V. Kamakoti, Director, Indian Institute of Technology Madras

Institutional Advisory Members (SCSVMV)

- Prof. Vempaty Kutumba Sastry, Chancellor, SCSVMV University
- Prof. G. Srinivasu, Vice-Chancellor, SCSVMV University
- Prof. R. Vasanth Kumar Mehta, Pro Vice-Chancellor
- Prof. G. Sriram, Registrar
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- Prof. Mark Rodwell, University of California Santa Barbara
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- Prof. Dong Eun Lee, Kyungpook National University
- Prof. Sirikanjana Thongme, Kasetsart University
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- Dr. P. Raja, DRDO
- Dr. Sameer Sharma, Former Chief Secretary, Government of Andhra Pradesh
- Shri N. Prabhakaran, Scientist 'G', GTRE, Bengaluru

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- Dr Ravi Bhatkal, Managing Director,MacDermid Alpha Electronics Solutions
- Mr Sharat Kaul,Global Advanced Packaging & OSAT Consultant
- Senior Technical Director, Intel India
- Senior Scientist, Texas Instruments India
- Principal Engineer, Qualcomm India
- Technology Expert, Applied Materials
- Senior Researcher, Micron Technology



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Prof. R. Sarathi, Dept. of EE and Dean (Planning), IITM

Prof. R. Harikrishnan, Dept. of Chemistry and
Director SJSCAR, SCSVMV

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- Dr. P. Venkatesan, Dept. of ECE
- Dr. K. Anisha, Dept. of EEE
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- Dr. S. Renuka Devi, Dept. of EEE
- Mr.T. Prakash, Dept. of CSE
- Mr. M. Chaitanya Kumar, Dept. of Management Studies

Pre-Conference Hands-on Workshops on 13 December 2026

- Power Device Technology
- Packaging Technology
- Advanced Materials
- Testing & Reliability
- Hands-on Labs and Demonstrations

TRACK A: SEMICONDUCTOR DEVICES
On 14 December 2026

- Wide Bandgap Semiconductor Materials and Epitaxy
- Power Semiconductor Device Design and Fabrication
- Emerging Applications of Power Semiconductor Devices
- Advanced Semiconductor Materials and Devices
- Advances in Power Semiconductor Devices

TRACK B – SPECIAL TRACKS / TOPICS

- AI & Computational Linguistics for the Semiconductor Industry
- Sanskrit & Ancient Indian Knowledge Systems for the Future of Hardware and Software Technologies
- Aligning Emerging Technology Requirements with Academic Curriculum in VLSI Design | Embedded Systems | AI/ML | Future Skills
- Sustainability Challenges in Semiconductor Fabrication Processes
- Supply Chain Challenges in Semiconductor Industry
- Critical Chemicals | Materials | Utilities | Compliance | Global Policy
- Education & Training for Building a Sustainable Semiconductor Workforce Towards the Viksit Bharat Semiconductor Ecosystem

TRACK C: SEMICONDUCTOR PACKAGING
On 15 December 2026

- Advanced Power Packaging Technology
- Advanced Packaging Methods
- Packaging Materials & Manufacturing
- Package Design, Modelling and Simulation
- Package Testing and Reliability
- Smart Manufacturing in Semiconductor Packaging

INDUSTRY & EMERGING TECHNOLOGIES

- Smart Manufacturing
- Semiconductor Manufacturing Technologies
- Advanced Materials and Reliability
- Design, Testing and Simulation
- Industry Applications and Technology Trends

Call for Papers

ICAST 2026 invites original research contributions, review articles, and industry papers covering all areas of semiconductor technologies. Authors are invited to submit their extended abstracts through the official conference website. Selected abstracts will be invited for oral presentation and based on the peer review the full papers will be published in INAE Transactions, subject to the applicable publication and editorial requirements.

Paper submission through

https://kanchiuniv.ac.in/icast2026/

IMPORTANT DATES

Abstract submission deadline :	25 October 2026
Acceptance Notification :	31 October 2026
Camera-Ready paper :	10 November 2026
Conference Dates :	13 -15 December 2026

Registration Fee Details

Category	Indian Participants	Foreign Participants
Students / Research Scholars	Rs.8000	USD 100
Faculty / Academicians	Rs.10000	USD 200
Industry Delegates	Rs.15000	USD 250

CONFERENCE MODE

Indian Participants: Physical Mode | Foreign Participants: Hybrid Mode

Venue

IIT MADRAS, ICSR Building, Chennai, India

CONTACT DETAILS

icast2026@kanchiuniv.ac.in

"Together, Let Us Shape the Future of Semiconductor Technology."