



श्री चन्द्रशेखरेन्द्र सरस्वती विश्वमहाविद्यालयः  
SRI CHANDRASEKHARENDR SARASWATHI VISWA MAHAVIDYALAYA  
(Accredited with 'A' Grade by NAAC)  
(Deemed to be University under section 3 of the UGC Act 1956)  
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**Dr. V.Ramanathan**  
Controller of Examinations (i/c)

Date: 12.11.2021

### NOTIFICATION

A public viva voce examination through **online mode** is scheduled on **25<sup>th</sup> November 2021**.  
The details are furnished below

<b>Candidate</b>	B.Kandavel, RM13EE45
<b>Department</b>	Electrical & Electronics Engineering
<b>Guide</b>	Dr.M.Manikandan Associate Professor, Department of ECE MIT, Anna University, Chennai- 600 044
<b>Topic</b>	<i>Harmonic Study on DFIG fed Inverter based WECS</i>
<b>Date and Time</b>	25.11.2021 @ 10.30 am

A copy of the Ph.D. thesis submitted by the scholar is kept in the library for reference.  
Research scholars and specialists in this field of research are cordially invited to attend the viva voce through **online mode** and take part in the discussion.

  
Controller of Examinations

Copy to:

1. The Honorable Vice Chancellor
2. The EC Desk
3. The Respected Registrar
4. All Deans & All Heads of the Departments, for favor of information and for display  
In the Notice Boards
5. The Finance Officer
6. The Librarian
7. The Research Guide
8. The Candidate

**CONTROLLER OF EXAMINATIONS**  
Sri Chandrasekharendra Saraswathi  
Viswa Mahavidyalaya  
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