श्री चन्द्रशेखरेन्द्र सरस्वती विश्वमहाविद्यालयः



SRI CHANDRASEKHARENDRA SARASWATHI VISWA MAHAVIDYALAYA

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Dr. V.Ramanathan
Controller of Examinations (i/c)

Date: 01.02.2022

NOTIFICATION

A public viva voce examination through **online mode** is scheduled on **14**th **February 2022**. The details are furnished below

Candidate	R.Arun Prasath, RM15EE52
Department	Electrical & Electronics Engineering
Guide	Dr.D.Vijayakumar Professor, School of Electrical Engineering VIT, Vellore - 632 014
Topic	Investigations on Multilevel Inverters for Photovoltaic System
Date and Time	14.02.2022 @ 10.30 am

A copy of the Ph.D. thesis submitted by the scholar is kept in the library for reference. Research scholars and specialists in this field of research are cordially invited to attend the viva voce through **online mode** and take part in the discussion.

Controller of Examinations (i/c)

CONTROLLER OF EXAMINATIONS
Sri Chandrasekharendra Saraswathi
Viswa Mahavidyalaya

Enathur Kanchipuram - 631 561.

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- 3. All Deans & All Heads of the Departments, for favor of information and for display in the Notice Boards
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- 6. The Research Guide
- 7. The Candidate