



श्री चन्द्रशेखरेन्द्र सरस्वती विश्वमहाविद्यालयः
SRI CHANDRASEKHARENDRA SARASWATHI VISWA MAHAVIDYALAYA
(Accredited with 'A' Grade by NAAC)
(Deemed to be University under section 3 of the UGC Act 1956)
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Dr. V.Ramanathan
Controller of Examinations (i/c)


Date: 03.02.2022

NOTIFICATION

A public viva voce examination through **online mode** is scheduled on **19th February 2022**.
The details are furnished below

Candidate	S.Renukadevi, RM14EE10
Department	Electrical & Electronics Engineering
Guide	Dr.M.Rathinakumar Professor & Head, Department of Electrical & Electronics Engineering SCSVMV, Enathur, Kanchipuram – 631 561
Topic	<i>An Improved Voltage SAG Mitigation Technique for Adjustable Speed Drives</i>
Date and Time	19.02.2022 @ 10.30 am

A copy of the Ph.D. thesis submitted by the scholar is kept in the library for reference. Research scholars and specialists in this field of research are cordially invited to attend the viva voce through **online mode** and take part in the discussion.


Controller of Examinations (i/c)
CONTROLLER OF EXAMINATIONS
Sri Chandrasekharendra Saraswathi
Viswa Mahavidyalaya
Enathur Kanchipuram - 631 561.

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