



श्रीचन्द्रशेखरेन्द्रसरस्वतीविश्वमहाविद्यालयः  
SRI CHANDRASEKHARENDRA SARASWATHI VISWA MAHAVIDYALAYA  
(Accredited with 'A' Grade by NAAC)  
(Deemed to be University under section 3 of the UGC Act 1956)  
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*Dr. V.Ramanathan*  
*Controller of Examinations (i/c)*


Date: 17.06.2022

**NOTIFICATION**

A public viva voce examination is scheduled on **30<sup>th</sup> June 2022**. The details are furnished below

<b>Candidate</b>	S.Dhandayuthapani, RM13EE21
<b>Department</b>	Electrical & Electronics Engineering
<b>Category</b>	Part-Time/ External
<b>Guide</b>	Dr.K.Anisha Assistant Professor, Department of Electrical & Electronics Engineering SCSVMV, Enathur, Kanchipuram
<b>Topic</b>	<i>Power Quality Improvement of Multibus Systems using Shunt Active Filter</i>
<b>Date and Time</b>	30.06.2022 @ 11.00 am
<b>Mode of Viva-Voce</b>	Physical Mode

A copy of the Ph.D. thesis submitted by the scholar is kept in the library for reference. Research scholars and specialists in this field of research are cordially invited to attend the viva voce in **Physical Mode** at the University campus, Enathur. You are informed to take part in the discussion.

  
Controller of Examinations (i/c)

**CONTROLLER OF EXAMINATIONS**  
**Sri Chandrasekharendra Saraswathi**  
**Viswa Mahavidyalaya**  
**Enathur Kanchipuram - 631 561**

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