श्रीचन्द्रशेखरेन्द्रसरस्वतीविश्वमहाविद्यालयः



SRI CHANDRASEKHARENDRA SARASWATHI VISWA MAHAVIDYALAYA

(Accredited with 'A' Grade by NAAC)
(Deemed to be University under section 3 of the UGC Act 1956)
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Dr. V.Ramanathan
Controller of Examinations (i/c)

Date: 02.11.2022

NOTIFICATION

A public viva voce examination is scheduled on 19th November 2022.

The details are furnished below

Candidate	Saravana Kumar. S.S (RM16EI015)
Department	Electronics & Instrumentation Engineering
Category	Part-Time/ Internal
Guide	Dr.K.Umapathy Associate Professor, Department of Electronics & Communication Engineering SCSVMV, Enathur, Kanchipuram – 631561
Topic	Interference Reduction and Load Balancing for Cellular Networks using OFDMA Technique
Date and Time	19.11.2022 @ 11.30 am
Mode of Viva-Voce	Physical Mode
Venue	Department of Electronics & Instrumentation Engineering, SCSVMV

Research scholars and specialists in this field of research are cordially invited to attend the viva voce in **Physical Mode.** You are informed to take part in the discussion.

Controller of Examinations (i/c)

CONTROLLER OF EXAMINATIONS
Sri Chandrasekharendra Saraswathi
Viswa Mahavidyalaya
Enathur Kanchipuram - 631 561

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