



श्रीचन्द्रशेखरेन्द्रसरस्वतीविश्वमहाविद्यालयः
SRI CHANDRASEKHARENDRA SARASWATHI VISWA MAHAVIDYALAYA
(Accredited with 'A' Grade by NAAC)
(Deemed to be University under section 3 of the UGC Act 1956)
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Dr. V.Ramanathan
Controller of Examinations (i/c)

Date: 06.02.2023

NOTIFICATION

A public viva voce examination is scheduled on **20th February 2023**.

The details are furnished below

Candidate	V.Malathi (RM15EE59)
Department	Electrical & Electronics Engineering
Category	Part-Time/ Internal
Guide	Dr.S.Sentamilselvan Associate Professor & Head, Department of Electrical & Electronics Engineering SCSVMV, Enathur, Kanchipuram-631561
Topic	<i>Design and Implementation of Impedance Source Inverter for Induction Motor Drive System</i>
Date and Time	20.02.2023 @ 10.30 am
Mode of Viva-Voce	Physical Mode
Venue	Department of Electrical & Electronics Engineering, SCSVMV

Research scholars and specialists in this field of research are cordially invited to attend the viva voce in **Physical Mode**. You are informed to take part in the discussion.

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Controller of Examinations (i/c)
CONTROLLER OF EXAMINATIONS
Sri Chandrasekharendra Saraswathi
Viswa Mahavidyalaya
Enathur, Kanchipuram - 631 561.

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