



श्रीचन्द्रशेखरेन्द्रसरस्वतीविश्वमहाविद्यालयः
SRI CHANDRASEKHARENDRA SARASWATHI VISWA MAHAVIDYALAYA
(Accredited with 'A' Grade by NAAC)
(Deemed to be University under section 3 of the UGC Act 1956)
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Mrs.S.Vijayalakshmi
Controller of Examinations

Date: 19.10.2023

NOTIFICATION

A public viva voce examination is scheduled on **04th November 2023**.

The details are furnished below

Candidate	V Uma
Department	Department of Electronics and Communication Engineering
Category	Part-Time/ External
Guide	Dr. M. A. Archana Assistant Professor Department of Electronics and Communication Engineering SCSVMV, Enathur, Kanchipuram
Topic	Formal Verification Techniques for Memory Management in Pipeline Architectures
Date and Time	04.11.2023 @ 11.00 a.m.
Mode of Viva-Voce	Physical Mode
Venue	Department of Electronics and Communication Engineering

Research scholars and specialists in this field of research are cordially invited to attend the viva voce in **Physical Mode** at the University campus, Enathur, Kanchipuram. You are informed to take part in the discussion.



S.Vijayalakshmi
Controller of Examinations

CONTROLLER OF EXAMINATIONS
Sri Chandrasekharendra Saraswathi
Viswa Mahavidyalaya
Enathur Kanchipuram - 631 561

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