



**SRI CHANDRASEKHARENDRASARASWATHI VISWA MAHA VIDYALAYA
(SCSVMV)**

(Deemed to be University U/S 3 of UGC Act 1956)

Accredited with "A" Grade by NAAC

Department of Electronics and Communication Engineering

TIME TABLE FOR III YEAR- VI SEMESTER (HALL NO: _____)

DAY/HOUR	9.10-10.00	10.00-10.50	10.50-11.40	11.50-12.40	12.40-1.30		2.20-3.10	3.10-4.00	4.00-4.50
MONDAY	CS	VLSI	CN	ES	POM	L	VLSI Design Lab		
TUESDAY	CN	CS	VLSI	ES	RS & GIS	U	Embedded Systems Design Lab		
WEDNESDAY	POM	RS & GIS	VLSI	CS	CN	N	ES	SOFT SKILL	
THURSDAY	RS & GIS	ES	POM	CN	VLSI	C	CS	Skill Development Course (Value Added Course)	
FRIDAY	ES	VLSI	CS	POM	RS & GIS	H	Computer Networks Lab		

SL.NO	SUBJECT CODE	SUBJECTS	FACULTY NAME
1	BECF186T10	Control Systems	Mr.R.Viswanath
2	BECF186T20	Computer Networks	Dr.K.Anitha
3	BECF186ED0	Professional Elective Course –II VLSI Design	Dr.G.SenthilKumar Kumar
4	BECF186OEF	Open Elective Course- II - Remote Sensing & GIS	Dr.T.Dinesh Kumar
5	BECF186T30	Principles of Management & Professional Ethics	Dr.K. Umopathy
6	BECF186T40	Embedded Systems	Mrs.G. Poornima
7	BETF18MC06	Soft Skills – IV*	Maths dept staff
8	BECF186P70	Computer Networks Lab	Dr.J.Vinothkumar
9	BECF186P80	Embedded Systems Design Lab	Mrs.G. Poornima
10	BECF186P90	VLSI Design Lab	Dr.K.Anitha
11		Skill Development Course (Value Added Course)	Dr.V.Jayapradha

CLASS – IN – CHARGE
Dr.S.Vijayaraghavan

HOD / ECE
(Dr.R.Jayalakshmi)