



**SRI CHANDRASEKHARENDRA SARASWATHI VISWA MAHA VIDYALAYA  
(SCSVMV)**

(Deemed to be University U/S 3 of UGC Act 1956)

Accredited with "A" Grade by NAAC

**Department of Electronics and Communication Engineering**

**TIME TABLE FOR ME – EST - I YEAR- II SEMESTER (HALL NO: 204)**

DAY/HOUR	10.00-10.50	10.50-11.40	11.50-12.40	12.40-1.30		2.20-3.10	3.10-4.00
MONDAY	RTOS	STES	ROB	ECSD	<b>L</b>	DIP	EN
TUESDAY	STES	<b>Embedded System Laboratory</b>			<b>U</b>	DIP	LIB
WEDNESDAY	RTOS	ROB	STES	EN	<b>N</b>	ECSD	DIP
THURSDAY	ECSD	STES	RTOS	ROB	<b>C</b>	EN	MOT
FRIDAY	EN	DIP	ROB	ECSD	<b>H</b>	RTOS	SEM

SL.NO	SUBJECT CODE	SUBJECTS	FACULTY NAME
		Software Technology For Embedded Systems	Dr.R.Jayalakshmi
		Real Time Operating System	Dr.K. Umapathy
		Embedded Communication And Software Design	Dr.S.Omkumar
		<b>Elective –II Principle of Robotics</b>	Dr.M.Vinoth
		<b>Elective – III - Digital Image Processing</b>	Dr.M.A.Archana
		Embedded Networking	Dr.S.Chandramohan
		<b>Embedded System Laboratory</b>	Dr.Jayapradha.V

CLASS – IN – CHARGE  
Dr.M.A.Archana

HOD / ECE  
(Dr.R.Jayalakshmi)