



# SRI CHANDRASEKHARENDRASARASWATHI VISWA MAHAVIDYALAYA

(Declared as Deemed-to-be University under Section 3 of the UGC Act, 1956, Vide notification No.F.9.9/92-U-3 dated 26<sup>th</sup> May 1993 of the Govt. of India)

ENATHUR, KANCHIPURAM - 631 561


## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING


EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)

Year	Semester	Section	Class Incharge
IV	VIII	S1	Dr.C.K.Gomathy

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
<b>MON</b>	Open Elective -II (Cyber Law and Ethics)	Open Elective -II (Cyber Law and Ethics)	Open Elective - III (Entrepreneurship)	Open Elective - III (Entrepreneurship)	Elective-VI (Image Processing)		Project Work Phase - II		
<b>TUE</b>	Elective-VI (Image Processing)	Elective-VI (Image Processing)	Open Elective - III (Entrepreneurship)	Open Elective -II (Cyber Law and Ethics)	Open Elective -II (Cyber Law and Ethics)		Project Work Phase - II		
<b>WED</b>	Open Elective - III (Entrepreneurship)	Open Elective - III (Entrepreneurship)	Open Elective -II (Cyber Law and Ethics)	Elective-VI (Image Processing)	Elective-VI (Image Processing)		Project Work Phase - II		
<b>THUR</b>	Project Work Phase - II						Project Work Phase - II		
<b>FRI</b>	Project Work Phase - II						Project Work Phase - II		

Subject Code	Subject Name	Faculty Name (Theory)
BCSF188EPO	Elective-VI (Image Processing)	Dr.K.Balachandran
BCSF188OEF	Open Elective -II (Cyber Law and Ethics)	Dr.C.K.Gomathy
BCSF188OEI	Open Elective -III (Entrepreneurship)	Dr.V.Geetha
BCSF188Z40	Project Work Phase - II	Dr.K.Balachandran

  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

  
Dr.P.Shanmugapriya  
Associate Head-CSE

  
Dr.M.Senthikumar  
HOD-CSE

  
Dr.M.Rathinakumar  
Dean - E & T



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ENATHUR, KANCHIPURAM - 631 561

## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)

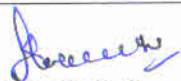
Year	Semester	Section	Class Incharge
III	VI	S1	Dr.V.Geetha

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
<b>MON</b>	<b>Honors</b> (Deep Learning / Ethical Hacking)		Elective - III (BI / DWM)	Computer Networks	Elective - II (CIS / CNS)		Computer Networks Lab (B-I) (Lab-2) Open CV Lab(B-II) (MAD Lab)		
<b>TUE</b>	<b>Honors</b> (Deep Learning Ethical Hacking)	OOAD	OOAD	Compiler Design	Computer Networks		Compiler Design Lab(B-II) (Lab-4) Open CV Lab(B-I) (Lab-3)		
<b>WED</b>	Computer Networks	OOAD	Elective - III (BI / DWM)	Elective - II (CIS / CNS)	Compiler Design		Elective - II (CIS / CNS)	Soft Skill-IV	
<b>THUR</b>	Elective - II (CIS / CNS)	<b>Honors</b> (Deep Learning Ethical Hacking)	OOAD	Compiler Design	OOAD		Elective - III (BI / DWM)	Compiler Design	Elective - III (BI / DWM)
<b>FRI</b>	Elective - III (BI / DWM)	Elective - II (CIS / CNS)	Compiler Design Lab(B-I) (Lab-2) Computer Networks Lab(B-II) (MAD Lab)				Computer Networks	Computer Networks	Compiler Design

Subject Code	Subject Name	Faculty Name
BCSF186T10	Compiler Design	Dr.V.Geetha
BCSF186T20	Computer Networks	Dr.M.Senthilkumaran
BCSF186T30	Object Oriented Analysis and Design	Ms.R.Radhika
BCSF186EFO	Elective - II (Cloud Infrastructure and Services)	Mr.D.Jeevan Kumar
BCSF186EIO	Elective - II (Cryptography and Network Security)	Dr.P.Shanmugapriya
BCSF186EHO	Elective -III (Business Intelligence)	Ms.D.Gayathri
BCSF186EG0	Elective -III (Data warehousing And Mining)	Mr.Harshavardhan
	Soft Skill - IV	


Subject Code	Subject Name	Faculty Name
BCSF186P60	Compiler Design Lab	Dr.V.Geetha
BCSF186P70	Computer Networks Lab	Mr.B.Karthikeyan
BCSF186P80	Open CV Lab	Dr.P.Shanmugapriya
BCSF186AI1	Deep Learning*	Dr.R.Prema
BCSF186CS1	Ethical Hacking and Digital Forensics*	Mr.V.Balu
BCSF186CS2	Forensics And Investigation Lab*	Mr.V.Balu
BCSF186Z90	Creative and Innovative Project	

\*Honors

  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

  
Dr.P.Shanmugapriya  
Associate Head-CSE

  
Dr.M.Senthilkumaran  
HOD-CSE

  
Dr.M.Rathinakumar  
Dean - E &T



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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)

Year	Semester	Section	Class Incharge
III	VI	S2	Dr.T.Lakshmbai

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
<b>MON</b>	Honors (Deep Learning / Ethical Hacking IOT /Robotics)		Elective - III (BI / DWM)	OOAD	Elective - II (CIS / CNS)/ Embedded System		Computer Networks	Soft Skill-IV	
<b>TUE</b>	Honors (Deep Learning Ethical Hacking IOT /Robotics)	OOAD	Computer Networks Lab (B-I) (Lab-2) Open CV Lab(B-II) (Dell Lab)				Compiler Design / OE-I(IPR)	Computer Networks	OOAD
<b>WED</b>	Compiler Design / OE-I(IPR)	Computer Networks	Elective - III (BI / DWM)	Elective - II (CIS / CNS)/ Embedded System	OOAD		Elective - II (CIS / CNS)/ Embedded System	Computer Networks	Compiler Design / OE-I(IPR)
<b>THUR</b>	Elective - II (CIS / CNS)/ Embedded System	Honors (Deep Learning Ethical Hacking IOT /Robotics)	Compiler Design Lab(B-II) (Lab-4) Object Oriented & System Design Lab-IT(Lab-4) Open CV Lab(B-I) (Dell Lab)				Elective - III (BI / DWM)	Compiler Design / OE-I(IPR)	Elective - III (BI / DWM)
<b>FRI</b>	Elective - III (BI / DWM)	Elective - II (CIS / CNS)/ Embedded System	Computer Networks	OOAD	Compiler Design / OE-I(IPR)		Compiler Design Lab(B-I) (Lab-4) Computer Networks Lab (B-II) (Lab-2)		

Subject Code	Subject Name	Faculty Name
BCSF186T10	Compiler Design	Dr.C.K.Gomathy
BCSF186T20	Computer Networks	Dr.T.Lakshmbai
BCSF186T30	Object Oriented Analysis and Design	Dr.M.Saraswathi
BCSF186EFO	Elective - II (Cloud Infrastructure and Services)	Mr.D Jeevan Kumar
BCSF186EIO	Elective - II (Cryptography and Network Security)	Dr.P.Shanmugapriya
BCSF186EHO	Elective -III (Business Intelligence)	Ms.D.Gayathri
BCSF186EGO	Elective -III (Data warehousing And Mining)	Mr.Harshavardhan
BITF186T10	Embedded System	Dr.T.Lakshmbai
BITF186OEA	Open Elective -I (Intellectual Property Rights)	Dr.K.Balachandran
	Soft Skill - IV	

Dr.T.Lakshmbai  
CSE-Timetable Coordinator

Dr.P.Shanmugapriya  
Associate Head-CSE

Subject Code	Subject Name	Faculty Name
BITF186P60	Object Oriented & System Design Lab	Dr.M.Saraswathi
BCSF186P60	Compiler Design Lab	Dr.C.K.Gomathy
BCSF186P70	Computer Networks Lab	Dr.T.Lakshmbai
BCSF186P80	Open CV Lab	Mr.D Jeevan Kumar
BCSF186AI1	Deep Learning*	Dr.R.Prema
BCSF186CS1	Ethical Hacking and Digital Forensics*	Mr.V.Balu
BCSF186CS2	Forensics And Investigation Lab*	Mr.V.Balu
Hons - IOT	Architecting smart IOT Devices *	Dr.R.Govindarajan
Hons-Robotics	Field and Service Robots*	Mr.Suresh
BCSF186Z90	Creative and Innovative Project	

\*Honors  
Dr.M.Senthil Kumar  
HOD-CSE

Dr.M.Rathnakumar  
Dean - E & T



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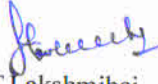
EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)


Year	Semester	Section	Class Incharge
II	IV	S1	Dr.T.Sundar

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
MON	Data Science for Engineers	Computer System Architecture	Design & Analysis of Algorithms Lab(B-I) (Lab-3) Microprocessor & Microcontroller Lab (B-II)				Design and Analysis of Algorithms	Operating Systems	Microprocessor and Microcontroller
TUE	Computer Architecture Lab(B-I) (Lab-2) Operating Systems Lab(B-II) (Lab-1)			Discrete Mathematics	Design and Analysis of Algorithms		Operating Systems	Discrete Mathematics	Sanskrit and Indian Culture-I
WED	Design and Analysis of Algorithms	Discrete Mathematics	Microprocessor and Microcontroller	Computer System Architecture	Operating Systems		Computer Architecture Lab(B-II) (Lab-2) Operating Systems Lab(B-I) (Lab-1)		
THUR	Computer System Architecture	Operating Systems	Data Science for Engineers	Discrete Mathematics	Microprocessor and Microcontroller		Design and Analysis of Algorithms Lab(B-II) (MAD Lab) Microprocessor and Microcontroller Lab(B-I)		
FRI	Discrete Mathematics	Microprocessor and Microcontroller	Design and Analysis of Algorithms	Computer System Architecture	Data Science for Engineers		Data Science Toolkit Lab (B-I & B-II) (Dell lab)		


Subject Code	Subject Name	Faculty Name
	Discrete Mathematics	Dr.K.Pramila
	Computer System Architecture	Dr.T.Sundar
	Microprocessor and Microcontroller	Dr.K.Anitha
	Design and Analysis of Algorithms	Ms.R.Preethi
	Operating Systems	Ms.D.Gayathri
	Sanskrit and Indian Culture-I	

Subject Code	Subject Name	Faculty Name
	Computer Architecture Lab	Dr.T.Sundar
	Microprocessor and Microcontroller Lab	Mr.J.Vinoth
	Design and Analysis of Algorithms Lab	Ms.R.Preethi
	Operating Systems Lab	Ms.D.Gayathri
	Data Science for Engineers *	Dr.K.Balachandran
	Data Science Toolkit Lab*	Dr.K.Balachandran Ms.R.Radhika

  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

  
Dr.P.Shanmugapriya  
Associate Head-CSE

  
Dr.M.Senthil Kumar  
HOD-CSE

  
Dr.M.Rathnakumar  
Dean - E & T



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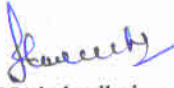
EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)

Year	Semester	Section	Class Incharge
II	IV	S2	Dr.T.Dineshkumar

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
MON	Design and Analysis of Algorithms	Data Science for Engineers	Discrete Mathematics	Microprocessor and Microcontroller	Operating Systems		Data Science Toolkit Lab (B-I & B-II) (Dell lab)		
TUE	Operating Systems	Design and Analysis of Algorithms	Data Science for Engineers	Computer System Architecture	Discrete Mathematics		Design and Analysis of Algorithms Lab(B-I) (MAD Lab) Microprocessor and Microcontroller Lab(B-II)		
WED	Computer System Architecture	Discrete Mathematics	Computer Architecture Lab(B-II) (Lab-2) Operating Systems Lab(B-I) (Lab-1)				Microprocessor and Microcontroller	Design and Analysis of Algorithms	Sanskrit and Indian Culture-I
THUR	Discrete Mathematics	Operating Systems	Design and Analysis of Algorithms Lab(B-II) (MAD Lab) Microprocessor and Microcontroller Lab(B-I)				Computer System Architecture	Discrete Mathematics	Microprocessor and Microcontroller
FRI	Microprocessor and Microcontroller	Data Science for Engineers	Computer Architecture Lab(B-I) (Lab-2) Operating Systems Lab(B-II) (Lab-1)				Design and Analysis of Algorithms	Operating Systems	Computer System Architecture


Subject Code	Subject Name	Faculty Name
	Discrete Mathematics	Dr.V.K.Radhakrishnan
	Computer System Architecture	Dr.T.Dineshkumar
	Microprocessor and Microcontroller	Dr.M.A.Archana
	Design and Analysis of Algorithms	Ms.R.Preethi
	Operating Systems	Mr.Harshwardhan
	Sanskrit and Indian Culture-I	

Subject Code	Subject Name	Faculty Name
	Computer Architecture Lab	Dr.T.Dineshkumar
	Microprocessor and Microcontroller Lab	Dr.M.A.Archana
	Design and Analysis of Algorithms Lab	Ms.R.Preethi
	Operating Systems Lab	Dr.M.Gayathri
	Data Science for Engineers *	Mr.E.Sankar
	Data Science Toolkit Lab*	Dr.V.Geetha / Dr.C.K.Gomathy

  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

  
Dr.P.Shanmugapriya  
Associate Head-CSE

  
Dr.M.Senthilkumar  
HOD-CSE

  
Dr.M.Rathinakumar  
Dean - E & T



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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING


EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)


Year	Semester	Section	Class Incharge
II	IV	S3	Dr.R.Sivaramakrishnan

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
MON	Operating Systems	Data Science for Engineers	Discrete Mathematics	Design and Analysis of Algorithms	Microprocessor and Microcontroller		Design & Analysis of Algorithms Lab (B-I)(LAB-4) Microprocessor and Microcontroller Lab(B-II)		
TUE	Computer System Architecture	Discrete Mathematics	Microprocessor and Microcontroller	Operating Systems	Discrete Mathematics		Computer Architecture Lab(B-II) (Lab-2) Operating Systems Lab(B-I) (Lab-1)		
WED	Design and Analysis of Algorithms	Computer System Architecture	Design and Analysis of Algorithms Lab(B-II) (MAD Lab) Microprocessor and Microcontroller Lab(B-I)				Operating Systems	Data Science for Engineers	Microprocessor and Microcontroller
THUR	Data Science for Engineers	Discrete Mathematics	Design and Analysis of Algorithms	Microprocessor and Microcontroller	Computer System Architecture		Data Science Toolkit Lab (B-I & B-II) (Dell lab)		
FRI	Discrete Mathematics	Computer System Architecture	Computer Architecture Lab(B-I) (Lab-2) Operating Systems Lab(B-II) (Lab-1)				Operating Systems	Design and Analysis of Algorithms	Sanskrit and Indian Culture-I


Subject Code	Subject Name	Faculty Name
	Discrete Mathematics	Dr.M.Varadha Raj
	Computer System Architecture	Dr.N.C.A.Boovarahan
	Microprocessor and Microcontroller	Dr.M.Vinoth
	Design and Analysis of Algorithms	Mr.SureshkumarBhadram
	Operating Systems	Dr.R.Sivaramakrishnan
	Sanskrit and Indian Culture-I	

Subject Code	Subject Name	Faculty Name
	Computer Architecture Lab	Dr.N.C.A.Boovarahan
	Microprocessor and Microcontroller Lab	Dr.M.Vinoth
	Design and Analysis of Algorithms Lab	Mr.SureshkumarBhadram
	Operating Systems Lab	Dr.R.Sivaramakrishnan
	Data Science for Engineers *	Ms.R.Radhika
	Data Science Toolkit Lab*	Ms.R.Radhika Mr.T.Pakash

  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

  
Dr.P.Shanmugapriya  
Associate Head-CSE

  
Dr.M.Senthil Kumar  
HOD-CSE

  
Dr.M.Rathinakumar  
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EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)

Year	Semester	Section	Class Incharge
II	IV	S4	Dr.D.Thamaraiselvi

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
MON	Operating Systems	Discrete Mathematics	Data Science Toolkit Lab (B-I & B-II) (Dell lab)				Computer System Architecture	Data Science for Engineers	Microprocessor and Microcontroller
TUE	Discrete Mathematics	Computer System Architecture	Design & Analysis of Algorithms Lab(B-I) (Lab-3) Microprocessor and Microcontroller Lab(B-II)				Data Science for Engineers	Design and Analysis of Algorithms	Operating Systems
WED	Design and Analysis of Algorithms	Operating Systems	Computer Architecture Lab(B-I) (Lab-2) Operating Systems Lab(B-II) (Lab-1)				Microprocessor and Microcontroller	Discrete Mathematics	Computer System Architecture
THUR	Microprocessor and Microcontroller	Discrete Mathematics	Design and Analysis of Algorithms	Discrete Mathematics	Sanskrit and Indian Culture-I		Computer Architecture Lab(B-II) (Lab-2) Operating Systems Lab(B-I) (Lab-1)		
FRI	Computer System Architecture	Design and Analysis of Algorithms	Data Science for Engineers	Operating Systems	Microprocessor and Microcontroller		Design & Analysis of Algorithms Lab(B-II)(Lab-3) Microprocessor and Microcontroller Lab(B-I)		

Subject Code	Subject Name	Faculty Name
	Discrete Mathematics	Dr.P.Balaji
	Computer System Architecture	Dr.R.Sivaramakrishnan
	Microprocessor and Microcontroller	Dr.S.Selvakumar
	Design and Analysis of Algorithms	Dr.D.Thamaraiselvi
	Operating Systems	Mr.R.Manickavasagam
	Sanskrit and Indian Culture-I	

Subject Code	Subject Name	Faculty Name
	Computer Architecture Lab	Dr.R.Sivaramakrishnan
	Microprocessor and Microcontroller Lab	Dr.S.Selvakumar
	Design and Analysis of Algorithms Lab	Dr.D.Thamaraiselvi
	Operating Systems Lab	Mr.R.Manickavasagam
	Data Science for Engineers *	Ms.R.Radhika
	Data Science Toolkit Lab*	Ms.R.Radhika Mr.T.Prakash

*Lakshmi*  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

*P. Shanmugapriya*  
Dr.P.Shanmugapriya  
Associate Head-CSE

*Senthil Kumar*  
Dr.M.Senthil Kumar  
HOD-CSE

*M. Rathinakumar*  
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## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)

Year	Semester	Section	Class Incharge
II	IV	S5	Mr.SureshkumarBhadram

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
<b>MON</b>	Discrete Mathematics	Design and Analysis of Algorithms	Computer Architecture Lab(B-II) (Lab-2) Operating Systems Lab(B-I) (Lab-1)				Introduction to Data Science	Operating Systems	Computer System Architecture
<b>TUE</b>	Design and Analysis of Algorithms	Computer System Architecture	Design and Analysis of Algorithms Lab(B-I) (MAD Lab) Microprocessor and Microcontroller Lab(B-II)				Operating Systems	Discrete Mathematics	Microprocessor and Microcontroller
<b>WED</b>	Introduction to Data Science	Operating Systems	Microprocessor and Microcontroller	Discrete Mathematics	Sanskrit and Indian Culture-I		Microprocessor and Microcontroller	Computer System Architecture	Design and Analysis of Algorithms
<b>THUR</b>	Computer Architecture Lab(B-I) (Lab-2) Operating Systems Lab(B-II) (Lab-1)			Discrete Mathematics	Operating Systems		Design and Analysis of Algorithms Lab(B-II) (MAD Lab) Microprocessor and Microcontroller Lab(B-I)		
<b>FRI</b>	Microprocessor and Microcontroller	Design and Analysis of Algorithms	R Programming Lab (B-I & B-II) (Dell lab)				Computer System Architecture	Discrete Mathematics	Introduction to Data Science

Subject Code	Subject Name	Faculty Name
	Discrete Mathematics	Dr.R.Mageswari
	Computer System Architecture	Dr.T.Dineshkumar
	Microprocessor and Microcontroller	Dr.R.Viswanath
	Design and Analysis of Algorithms	Mr.SureshkumarBhadram
	Operating Systems	Dr.M.Gayathri
	Sanskrit and Indian Culture-I	

*[Signature]*  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

*[Signature]*  
Dr.P.Shanmugapriya  
Associate Head-CSE

Subject Code	Subject Name	Faculty Name
	Computer Architecture Lab	Dr.T.Dineshkumar
	Microprocessor and Microcontroller Lab	
	Design and Analysis of Algorithms Lab	Mr.SureshkumarBhadram
	Operating Systems Lab	Dr.M.Gayathri
	Introduction to Data Science *	Mr.D.Jeevan Kumar
	R Programming Lab*	Mr.D.Jeevan Kumar Ms.R.Rajalakshmi

*[Signature]*  
Dr.M.Senthil Kumar  
HOD-CSE

*[Signature]*  
Dr.M.Rathinakumar  
Dean - E & T



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EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)

Year	Semester	Section	Class Incharge
II	IV	S6	Mr.E.Sankar

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
<b>MON</b>	Microprocessor and Microcontroller	Discrete Mathematics	Design and Analysis of Algorithms Lab(B-I) (MAD Lab) Microprocessor and Microcontroller Lab(B-II)				Computer System Architecture	Design and Analysis of Algorithms	Operating Systems
<b>TUE</b>	Discrete Mathematics	Microprocessor and Microcontroller	Operating Systems	Introduction to Data Science	Design and Analysis of Algorithms		Data Science Toolkit Lab (AIML) (Lab-3) / R Programming Lab (AIDS) (Dell lab)		
<b>WED</b>	Design and Analysis of Algorithms	Computer System Architecture	Discrete Mathematics	Microprocessor and Microcontroller	Computer System Architecture		Design and Analysis of Algorithms Lab(B-I) (MAD Lab) Microprocessor and Microcontroller Lab(B-II)		
<b>THUR</b>	Operating Systems	Discrete Mathematics	Computer Architecture Lab(B-II) (Lab-2) Operating Systems Lab(B-I) (Lab-1)				Introduction to Data Science	Computer System Architecture	Microprocessor and Microcontroller
<b>FRI</b>	Introduction to Data Science	Operating Systems	Discrete Mathematics	Design and Analysis of Algorithms	Sanskrit and Indian Culture-I		Computer Architecture Lab(B-II) (Lab-2) Operating Systems Lab(B-I) (Lab-1)		

Subject Code	Subject Name	Faculty Name
	Discrete Mathematics	Dr.D.Vijayalakshmi
	Computer System Architecture	Dr.T.Sundar
	Microprocessor and Microcontroller	Dr.P.Rajalakshmi
	Design and Analysis of Algorithms	Mr.E.Sankar
	Operating Systems	Mr.R.Manickavasagam
	Sanskrit and Indian Culture-I	

Subject Code	Subject Name	Faculty Name
	Computer Architecture Lab	Dr.T.Sundar
	Microprocessor and Microcontroller Lab	Dr.K.Anisha
	Design and Analysis of Algorithms Lab	Mr.E.Sankar
	Operating Systems Lab	Mr.R.Manickavasagam
	Introduction to Data Science *	Ms.R.Rajalakshmi
	R Programming Lab*	Ms.R.Rajalakshmi Mr.D.Jeevan Kumar

*[Signature]*  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

*[Signature]*  
13/01/26 :  
Dr.P.Shanmugapriya  
Associate Head-CSE

*[Signature]*  
Dr.M.Senthil Kumar  
HOD-CSE

*[Signature]*  
13/01/26  
Dr.M.Rathnakumar  
Dean - E & T



# SRI CHANDRASEKHARENDRA SARASWATHI VISWA MAHAVIDYALAYA

(Declared as Deemed-to-be University under Section 3 of the UGC Act, 1956, Vide notification No.F.9/92-U-3 dated 26<sup>th</sup> May 1993 of the Govt. of India)

ENATHUR, KANCHIPURAM – 631 561

## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

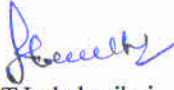
EVEN SEMESTER - TIME TABLE (JAN 2026 – MAY 2026)

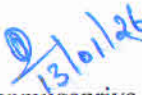
Year	Semester	Section	Class Incharge
II	IV	S7	Dr.N.C.A.Boovarahan

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
MON	Computer System Architecture	Design and Analysis of Algorithms	Computer Architecture Lab(B-II) (Lab-2) Operating Systems Lab(B-I) (Lab-1)				Operating Systems	Discrete Mathematics	Design and Analysis of Algorithms
TUE	Microprocessor and Microcontroller	Operating Systems	Computer System Architecture	Honors	Microprocessor and Microcontroller		Honors Theory/Lab AIML-DST Lab-3/AIDS – R Prog – Dell Lab IOT – IOT lab		
WED	Operating Systems	Discrete Mathematics	Design and Analysis of Algorithms	Microprocessor and Microcontroller	Computer System Architecture		Computer Architecture Lab(B-II) (Lab-2) Operating Systems Lab(B-I) (Lab-1)		
THUR	Design and Analysis of Algorithms	Discrete Mathematics	Design & Analysis of Algorithms Lab(B-I) (Lab-3) Microprocessor and Microcontroller Lab(B-II)				Honors	Computer System Architecture	Sanskrit and Indian Culture-I
FRI	Honors	Discrete Mathematics	Microprocessor and Microcontroller	Discrete Mathematics	Operating Systems		Design & Analysis of Algorithms Lab(B-II) (MAD Lab) Microprocessor and Microcontroller Lab(B-I)		

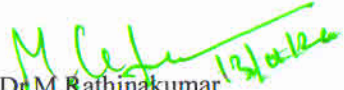
Subject Code	Subject Name	Faculty Name
	Discrete Mathematics	Dr.E.Geetha
	Computer System Architecture	Dr.N.C.A.Boovarahan
	Microprocessor and Microcontroller	Dr.S.Bharathi
	Design and Analysis of Algorithms	Dr.D.Thamaraiselvi
	Operating Systems	Ms.S.E.Viswapriya
	Sanskrit and Indian Culture-I	

Subject Code	Subject Name	Faculty Name
	Computer Architecture Lab	Dr.N.C.A.Boovarahan
	Microprocessor and Microcontroller Lab	Dr.V.Malathi
	Design and Analysis of Algorithms Lab	Mr.E.Sankar
	Operating Systems Lab	Ms.S.E.Viswapriya
Cybersecurity	Security Audit and Risk Assessment*	Mr.B.Karthikeyan
IOT	Embedded Systems and Programs*	Dr.R.Govindarajan

  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

  
Dr.P.Shanmugapriya  
Associate Head-CSE

  
Dr.M.Senthil Kumar  
HOD-CSE

  
Dr.M.Rathin Kumar  
Dean – E & T



# SRI CHANDRASEKHARENDRASARASWATHI VISWA MAHAVIDYALAYA

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ENATHUR, KANCHIPURAM - 631 561

## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)

Year	Semester	Section	Class Incharge
I	II	S1	Mr.Jeevan Kumar D

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
MON	Design Thinking	Universal Human Values	Engineering Mathematics - II	Environmental Science and Engineering	Engineering Physics		Design Thinking and IDEA Lab		
TUE	Universal Human Values	Engineering Physics	Engineering Mathematics - II	Design Thinking	Yoga		English		
WED	Environmental Science and Engineering	Engineering Mathematics - II	Design Thinking	Design Thinking	English		IDEA Lab		
THUR	Engineering Mathematics - II	Yoga	Engineering Physics	Soft Skill			English	Engineering Physics	LIB
FRI	Universal Human Values	Environmental Science and Engineering	Engineering Physics	Engineering Mathematics - II	Design Thinking		Engineering Physics Lab		

Subject Code	Subject Name	Faculty Name
	English	
	Engineering Mathematics - II	Dr.D.Vijayalakshmi
	Universal Human Values	Dr.S.Devika
	Environmental Science and Engineering	

*Lakshmi*  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

*13/01/26*  
Dr.P.Shanmugapriya  
Associate Head-CSE

Subject Code	Subject Name	Faculty Name
T&P	Engineering Physics	Dr.M.Sundarrajan
T&P	Design Thinking and IDEA Lab	Dr.D.Vijayan
	Yoga	
	Soft Skill	Ms.Aishwarya

*M.S.*  
Dr.M.Senthilkumaran  
HOD-CSE

*Rathinakumar*  
Dr.M.Rathinakumar  
Dean - E & T



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ENATHUR, KANCHIPURAM - 631 561

## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

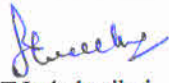
EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)

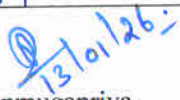
Year	Semester	Section	Class Incharge
I	II	S2	Dr.M.Gayathri

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
MON	Universal Human Values	Engineering Mathematics - II	Design Thinking	Environmental Science and Engineering	English		Engineering Physics Lab		
TUE	Design Thinking	Engineering Mathematics - II	Universal Human Values	Engineering Physics	LIB		Design Thinking and IDEA Lab		
WED	Engineering Mathematics - II	Environmental Science and Engineering	Universal Human Values	Design Thinking	Yoga		English		
THUR	Engineering Physics	Design Thinking	Engineering Mathematics - II	Yoga	Engineering Physics		Design Thinking and IDEA Lab		
FRI	Environmental Science and Engineering	Engineering Physics	Engineering Mathematics - II	Soft Skill			English	Engineering Physics	Design Thinking

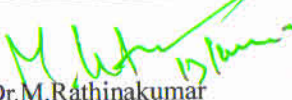
Subject Code	Subject Name	Faculty Name
	English	
	Engineering Mathematics - II	Dr.R.Mageswari
	Universal Human Values	
	Environmental Science and Engineering	

Subject Code	Subject Name	Faculty Name
T&P	Engineering Physics	Dr.R.Raja
T&P	Design Thinking and IDEA Lab	Dr.P.Vithya
	Yoga	
	Soft Skill	Ms.Aishwarya

  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

  
Dr.P.Shanmugapriya  
Associate Head-CSE

  
Dr.M.Senthil Kumar  
HOD-CSE

  
Dr.M.Rathinakumar  
Dean - E & T



# SRI CHANDRASEKHARENDRASARASWATHI VISWA MAHAVIDYALAYA

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ENATHUR, KANCHIPURAM – 631 561

## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

EVEN SEMESTER - TIME TABLE (JAN 2026 – MAY 2026)

Year	Semester	Section	Class Incharge
I	II	S3	Ms.R.Radhika

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
<b>MON</b>	Design Thinking	Universal Human Values	Engineering Physics	Soft Skill			English	Yoga	LIB
<b>TUE</b>	Engineering Physics	Engineering Mathematics – II	Design Thinking	Universal Human Values	Environmental Science and Engineering		Engineering Physics Lab		
<b>WED</b>	Engineering Mathematics – II	Environmental Science and Engineering	Universal Human Values	Engineering Physics	Design Thinking		Design Thinking and IDEA Lab		
<b>THUR</b>	Engineering Mathematics – II	Design Thinking	Yoga	Engineering Physics	English		English		
<b>FRI</b>	Environmental Science and Engineering	Design Thinking	Engineering Mathematics – II	Engineering Mathematics – II	Engineering Physics		Design Thinking and IDEA Lab		

Subject Code	Subject Name	Faculty Name
	English	
	Engineering Mathematics – II	Dr.A.Gayathri
	Universal Human Values	Dr.P.Vithya
	Environmental Science and Engineering	

*Dr. T. Lakshmi Bai*  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

*Dr. P. Shanmugapriya*  
Dr.P.Shanmugapriya  
Associate Head-CSE

Subject Code	Subject Name	Faculty Name
T&P	Engineering Physics	Dr.J.Suganthi
T&P	Design Thinking and IDEA Lab	Dr. G. Venkatakoteswararao
	Yoga	
	Soft Skill	Ms.Aishwarya

*Dr. M. Senthil Kumar*  
Dr.M.Senthil Kumar  
HOD-CSE

*Dr. M. Rathin Kumar*  
Dr.M.Rathin Kumar  
Dean – E & T



# SRI CHANDRASEKHARENDRASARASWATHI VISWA MAHAVIDYALAYA

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ENATHUR, KANCHIPURAM - 631 561

## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)

Year	Semester	Section	Class Incharge
I	II	S4	Ms.R.Preethi

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
<b>MON</b>	Environment al Science and Engineering	Engineering Mathematics - II	Universal Human Values	Engineering Physics	Design Thinking		Design Thinking and IDEA Lab		
<b>TUE</b>	Engineering Mathematics - II	Engineering Physics	Design Thinking	Soft Skill			English	Yoga	LIB
<b>WED</b>	Universal Human Values	Environment al Science and Engineering	Engineering Mathematics - II	Design Thinking	Engineering Physics		Engineering Physics Lab		
<b>THUR</b>	Engineering Physics	Design Thinking	Engineering Mathematics - II	English	YOGA		Design Thinking and IDEA Lab		
<b>FRI</b>	Design Thinking	Universal Human Values	Environment al Science and Engineering	Engineering Physics	Engineering Mathematics - II		English		

Subject Code	Subject Name	Faculty Name
	English	
	Engineering Mathematics - II	Dr.M.Varadha Raj
	Universal Human Values	
	Environmental Science and Engineering	

Subject Code	Subject Name	Faculty Name
T&P	Engineering Physics	Dr.J.Suganthi
T&P	Design Thinking and IDEA Lab	Dr.R.Balakumar
	Yoga	
	Soft Skill	Ms.Aishwarya

*Preethi*  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

*Dr.P.Shanmugapriya*  
Dr.P.Shanmugapriya  
Associate Head-CSE

*Dr.M.Senthilkumaran*  
Dr.M.Senthilkumaran  
HOD-CSE

*Dr.M.Rathinakumar*  
Dr.M.Rathinakumar  
Dean - E & T



# SRI CHANDRASEKHARENDRASARASWATHI VISWA MAHAVIDYALAYA

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ENATHUR, KANCHIPURAM – 631 561

## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

EVEN SEMESTER - TIME TABLE (JAN 2026 – MAY 2026)

Year	Semester	Section	Class Incharge
I	II	S5	Ms.D.Gayathri

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
MON	Environmental Science and Engineering	Universal Human Values	Engineering Mathematics - II	Engineering Physics	Yoga		English		
TUE	Universal Human Values	Engineering Mathematics - II	Engineering Physics	Design Thinking	LIB		Design Thinking and IDEA Lab		
WED	Engineering Mathematics - II	Design Thinking	Engineering Physics	Soft Skill			English	Engineering Physics	English
THUR	Yoga	Environmental Science and Engineering	Design Thinking	Engineering Physics	Engineering Mathematics - II		Engineering Physics Lab		
FRI	Universal Human Values	Environmental Science and Engineering	Design Thinking	Engineering Mathematics - II	Design Thinking		Design Thinking and IDEA Lab		

Subject Code	Subject Name	Faculty Name
	English	
	Engineering Mathematics - II	Dr.P.Balaji
	Universal Human Values	Dr.R.Sumathi
	Environmental Science and Engineering	

Subject Code	Subject Name	Faculty Name
T&P	Engineering Physics	Mr.V.Akshaya Shiva
T&P	Design Thinking and IDEA Lab	Mr.K.Suresh
	Yoga	
	Soft Skill	Ms.Aishwarya

*[Signature]*  
Dr.T.Lakshmibai  
CSE-Timetable Coordinator

*[Signature]*  
Dr.P.Shanmugapriya  
Associate Head-CSE

*[Signature]*  
Dr.M.Senthilkumaran  
HOD-CSE

*[Signature]*  
Dr.M.Rathinakumar  
Dean - E & T



# SRI CHANDRASEKHARENDRASARASWATHI VISWA MAHAVIDYALAYA

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ENATHUR, KANCHIPURAM - 631 561

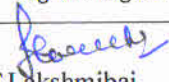
## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)

Year	Semester	Section	Class Incharge
I	II	S6	Mr.V.Balu

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
<b>MON</b>	Engineering Mathematics - II	Basic Electrical Engineering	Basic Electrical Engineering Lab				Engineering Chemistry	Programming for Problem Solving	Soft Skill
<b>TUE</b>	Basic Electrical Engineering	Engineering Chemistry	Engineering Mathematics - II	Programming for Problem Solving	Sem		Engineering Graphics and Design		
<b>WED</b>	Programming for Problem Solving (Lab-3)	Engineering Mathematics - II	Engineering Graphics and Design				Basic Electrical Engineering	Yoga	Engineering Chemistry
<b>THUR</b>	Engineering Chemistry	Basic Electrical Engineering	Engineering Mathematics - II	Programming for Problem Solving	Programming for Problem Solving		Engineering Chemistry Lab		
<b>FRI</b>	Engineering Mathematics - II	Basic Electrical Engineering	Programming for Problem Solving Lab (Computer Lab-3)				Programming for Problem Solving	Engineering Chemistry	Lib

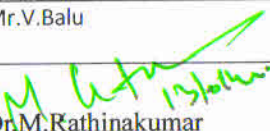
Subject Code	Subject Name	Faculty Name
	Engineering Mathematics - II	Dr.K.Bharathi
	Engineering Chemistry	Ms.M.Paramaeswari
	Basic Electrical Engineering	Dr.R.Malathi
	Programming for Problem Solving	Mr.V.Balu
	Engineering Graphics and Design	Mr.K.Vasu / Mr.V.Johnsundarraji

  
 Dr.T.Lakshmi Bai  
 CSE-Timetable Coordinator

  
 Dr.P.Shanmugapriya  
 Associate Head-CSE

Subject Code	Subject Name	Faculty Name
	Basic Electrical Engineering Lab	Dr.R.Malathi / Dr.S.Lavanya
	Programming for Problem Solving Lab	Mr.V.Balu / Ms.D.Gayathri
	Yoga	
	Soft Skill	Ms.Aishwarya
	Library & Seminar	Mr.V.Balu

  
 Dr.M.Senthilkumar  
 HOD-CSE

  
 Dr.M.Rathinakumar  
 Dean - E & T



# SRI CHANDRASEKHARENDRASARASWATHI VISWA MAHAVIDYALAYA

(Declared as Deemed-to-be University under Section 3 of the UGC Act, 1956, Vide notification No.F.9/92-U-3 dated 26<sup>th</sup> May 1993 of the Govt. of India)

ENATHUR, KANCHIPURAM - 631 561

## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)

Year	Semester	Section	Class Incharge
I	II	S7	Dr.R.Prema

Time / period	1	2	3	4	5	01.30 to 2.20	6	7	8
MON	9.10 to 10.00 Basic Electrical Engineering	10.00 to 10.50 Engineering Mathematics - II	10.50 to 11.40 11.50 to 12.40 12.40 to 01.30 Engineering Graphics and Design			01.30 to 2.20	2.20 to 03.10 Programming for Problem Solving	3.10 to 04.00 Engineering Chemistry	04.00 to 04.50 Yoga
TUE	9.10 to 10.00 Basic Electrical Engineering	10.00 to 10.50 Programming for Problem Solving	10.50 to 11.40 11.50 to 12.40 12.40 to 01.30 Basic Electrical Engineering Lab				2.20 to 03.10 Engineering Mathematics - II	3.10 to 04.00 Engineering Chemistry	04.00 to 04.50 Soft Skill
WED	9.10 to 10.00 Engineering Chemistry	10.00 to 10.50 Basic Electrical Engineering	10.50 to 11.40 Programming for Problem Solving	11.50 to 12.40 Basic Electrical Engineering	12.40 to 01.30 Engineering Chemistry		04.00 to 04.50 Programming for Problem Solving Lab (Computer Lab-3)		
THUR	9.10 to 10.00 Engineering Mathematics - II	10.00 to 10.50 Engineering Chemistry	10.50 to 11.40 Basic Electrical Engineering	11.50 to 12.40 Programming for Problem Solving	12.40 to 01.30 Lib		04.00 to 04.50 Engineering Graphics and Design		
FRI	9.10 to 10.00 Programming for Problem Solving (Lab-3)	10.00 to 10.50 Programming for Problem Solving (Lab-3)	10.50 to 11.40 Engineering Mathematics - II	11.50 to 12.40 Engineering Mathematics - II	12.40 to 01.30 Sem		04.00 to 04.50 Engineering Chemistry Lab		

Subject Code	Subject Name	Faculty Name
	Engineering Mathematics - II	Dr.T.N.Kavitha
	Engineering Chemistry	Dr.R.Manjupriya
	Basic Electrical Engineering	Dr.S.Lavanya
	Programming for Problem Solving	Dr.R.Prema
	Engineering Graphics and Design	Mr.K.Vasu / Mr.M.Vimalanathan

Subject Code	Subject Name	Faculty Name
	Basic Electrical Engineering Lab	Dr.S.Lavanya / Mr.M.Mahendran
	Programming for Problem Solving Lab	Dr.R.Prema / Dr.M.Saraswathi
	Yoga	
	Soft Skill	Ms.Aishwarya
	Library & Seminar	Dr.R.Prema

*Dr. T. Lakshmi Bai*  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

*Dr. P. Shanmugapriya*  
Dr.P.Shanmugapriya  
Associate Head-CSE

*Dr. M. Senthil Kumar*  
Dr.M.Senthil Kumar  
HOD-CSE

*Dr. M. Rathinakumar*  
Dr.M.Rathinakumar  
Dean - E & T



# SRI CHANDRASEKHARENDR SARASWATHI VISWA MAHAVIDYALAYA

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ENATHUR, KANCHIPURAM - 631 561

## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)

Year	Semester	Section	Class Incharge
I	II	S8	Ms.S.E.Viswapriya

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
MON	Basic Electrical Engineering	Programming for Problem Solving	Engineering Mathematics - II	Engineering Chemistry	Basic Electrical Engineering		Engineering Chemistry Lab		
TUE	Engineering Chemistry	Engineering Mathematics - II	Basic Electrical Engineering	Programming for Problem Solving	Engineering Mathematics - II		Engineering Chemistry	Soft Skill	Yoga
WED	Engineering Mathematics - II	Engineering Chemistry	Basic Electrical Engineering Lab				Engineering Graphics and Design		
THUR	Programming for Problem Solving (Lab-3)	Programming for Problem Solving (Lab-3)	Basic Electrical Engineering	Engineering Mathematics - II	Sem		Programming for Problem Solving Lab (Computer Lab-3)		
FRI	Programming for Problem Solving	Basic Electrical Engineering	Programming for Problem Solving	Engineering Chemistry	LIB		Engineering Graphics and Design		

Subject Code	Subject Name	Faculty Name
	Engineering Mathematics - II	Dr.T.N.Kavitha
	Engineering Chemistry	Ms.V.Vaidehi
	Basic Electrical Engineering	Dr.S.Renukadevi
	Programming for Problem Solving	Ms.S.E.Viswapriya
	Engineering Graphics and Design	Ms.P.Punitha / Mr.M.Vimalanathan

Subject Code	Subject Name	Faculty Name
	Basic Electrical Engineering Lab	Dr.S.Renukadevi / Mr.M.Mahendran
	Programming for Problem Solving Lab	Ms.S.E.Viswapriya / Mr.V.Balu
	Yoga	
	Soft Skill	Ms.Aishwarya
	Library & Seminar	Ms.S.E.Viswapriya

*[Signature]*  
Dr.T.Lakshmbai  
CSE-Timetable Coordinator

*[Signature]*  
13/01/26  
Dr.P.Shanmugapriya  
Associate Head-CSE

*[Signature]*  
Dr.M.Senthilkumar  
HOD-CSE

*[Signature]*  
Dr.M.Rathinakumar  
Dean - E & T



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ENATHUR, KANCHIPURAM - 631 561

## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

*EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)*

Year	Semester	Section	Class Incharge
I	II	S9	Ms.R.Rajalakshmi

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
<b>MON</b>	Basic Electrical Engineering	Engineering Chemistry	Engineering Mathematics - II	Programming for Problem Solving	Lib		Engineering Graphics and Design		
<b>TUE</b>	Programming for Problem Solving (Lab-3)	Programming for Problem Solving (Lab-3)	Basic Electrical Engineering	Engineering Mathematics - II	Engineering Chemistry		Engineering Chemistry Lab		
<b>WED</b>	Basic Electrical Engineering	Engineering Mathematics - II	Programming for Problem Solving Lab (Computer Lab-3)				Programming for Problem Solving	Engineering Chemistry	Yoga
<b>THUR</b>	Engineering Chemistry	Programming for Problem Solving	Basic Electrical Engineering Lab				Basic Electrical Engineering	Engineering Mathematics - II	Soft Skill
<b>FRI</b>	Engineering Mathematics - II	Basic Electrical Engineering	Engineering Graphics and Design				Engineering Chemistry	Programming for Problem Solving	Sem

Subject Code	Subject Name	Faculty Name
	Engineering Mathematics - II	Dr.A.Gayathri
	Engineering Chemistry	Mr.V.Santhanam
	Basic Electrical Engineering	Dr.S.Raja
	Programming for Problem Solving	Ms.R.Rajalakshmi
	Engineering Graphics and Design	Dr.M.Sureshkumar/ Mr.V.Johnsundarraaj

Subject Code	Subject Name	Faculty Name
	Basic Electrical Engineering Lab	Dr.S.Raja / Ms.Mahalakshmi
	Programming for Problem Solving Lab	Ms.R.Rajalakshmi / Mr.V.Balu
	Yoga	
	Soft Skill	Ms.Aishwarya
	Library & Seminar	Ms.R.Rajalakshmi

*[Signature]*  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

*[Signature]*  
Dr.P.Shanmugapriya  
Associate Head-CSE

*[Signature]*  
Dr.M.Senthilkumar  
HOD-CSE

*[Signature]*  
Dr.M.Rathinakumar  
Dean - E & T



# SRI CHANDRASEKHARENDRASARASWATHI VISWA MAHAVIDYALAYA

(Declared as Deemed-to-be University under Section 3 of the UGC Act, 1956, Vide notification No.F.9/92-U-3 dated 26<sup>th</sup> May 1993 of the Govt. of India)

ENATHUR, KANCHIPURAM – 631 561

## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

### EVEN SEMESTER - TIME TABLE (JAN 2026 – MAY 2026)

Year	Semester	Section	Class Incharge
I	II	S10	Dr.M.Saraswathi

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
<b>MON</b>	Programming for Problem Solving (Lab-3)	Programming for Problem Solving (Lab-3)	Engineering Mathematics - II	Basic Electrical Engineering	Lib		Programming for Problem Solving Lab (Computer Lab-3)		
<b>TUE</b>	Engineering Mathematics - II	Basic Electrical Engineering	Engineering Graphics and Design				Programming for Problem Solving	Engineering Chemistry	Yoga
<b>WED</b>	Basic Electrical Engineering	Engineering Chemistry	Programming for Problem Solving	Basic Electrical Engineering	Engineering Mathematics - II		Engineering Chemistry Lab		
<b>THUR</b>	Programming for Problem Solving	Engineering Mathematics - II	Engineering Graphics and Design				Engineering Chemistry	Basic Electrical Engineering	Sem
<b>FRI</b>	Engineering Chemistry	Programming for Problem Solving	Basic Electrical Engineering Lab				Engineering Mathematics - II	Engineering Chemistry	Soft Skill

Subject Code	Subject Name	Faculty Name
	Engineering Mathematics – II	Dr.K.Pramila
	Engineering Chemistry	Dr.B.Muthukumaran
	Basic Electrical Engineering	Dr.V.Malathi
	Programming for Problem Solving	Dr.M.Saraswathi
	Engineering Graphics and Design	Dr.M.Sureshkumar/ Ms.P.Punitha

*[Signature]*  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

*[Signature]*  
13/01/26  
Dr.P.Shanmugapriya  
Associate Head-CSE

Subject Code	Subject Name	Faculty Name
	Basic Electrical Engineering Lab	Dr.V.Malathi / Dr.N.Ashokkumar
	Programming for Problem Solving Lab	Dr.M.Saraswathi / Ms.R.Rajalakshmi
	Yoga	
	Soft Skill	Ms.Aishwarya
	Library & Seminar	Dr.M.Saraswathi

*[Signature]*  
Dr.M.Senthilkumaran  
HOD-CSE

*[Signature]*  
Dr.M.Rathinakumar  
Dean – E & T



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ENATHUR, KANCHIPURAM - 631 561


## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

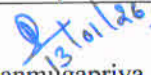
EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)

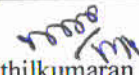
Year	Semester	Class Incharge
I ME-Regular	II	Dr.P.Shanmugapriya


Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
<b>MON</b>	Data Analytics	Elective-III (Human and Computer Interaction)	Elective-III (Human and Computer Interaction)	Data Analytics			Mini Project with Seminar		
<b>TUE</b>	Elective-III	Data Analytics	Elective-II (Computer Vision)	Elective-II (Computer Vision)			Mini Project with Seminar		
<b>WED</b>	Data Analytics Lab			Elective-II (Computer Vision)			Mini Project with Seminar		
<b>THUR</b>	Elective-II (Computer Vision)	Data Analytics	Elective-III (Human and Computer Interaction)	Elective-III (Human and Computer Interaction)			Soft Computing	Soft Computing	Data Analytics
<b>FRI</b>	Soft Computing	Human and Computer Interaction Lab					Soft Computing	Soft Computing	Elective-II (Computer Vision)

Subject Code	Subject Name	Faculty Name
	Data Analytics	Dr.P.Shanmugapriya
	Soft Computing	Dr.M.Gayathri
	Elective-II (Computer Vision)	Dr.R.Prema
	Elective-III (Human and Computer Interaction)	Dr.T.Lakshmi Bai
	Data Analytics Lab	Dr.P.Shanmugapriya
	Human and Computer Interaction Lab	Mr.Harshawardhan
	Mini Project with Seminar	Dr.M.Senthilkumar

  
Dr.T.Lakshmi Bai  
CSE-Timetable Coordinator

  
Dr.P.Shanmugapriya  
Associate Head-CSE

  
Dr.M.Senthilkumar  
HOD-CSE

  
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ENATHUR, KANCHIPURAM - 631 561


## DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

EVEN SEMESTER - TIME TABLE (JAN 2026 - MAY 2026)

Year	Semester	Class Incharge
I ME Part Time	II	Dr.P.Shanmugapriya

Time / period	1 9.10 to 10.00	2 10.00 to 10.50	3 10.50 to 11.40	4 11.50 to 12.40	5 12.40 to 01.30	01.30 to 2.20	6 2.20 to 03.10	7 3.10 to 04.00	8 04.00 to 04.50
MON									
TUE									
WED									
THUR	Data Science Lab			Advanced Computer Architecture			Soft Computing	Soft Computing	
FRI	Soft Computing	Elective-I (Data Science)	Elective-I (Data Science)	Advanced Computer Architecture			Soft Computing	Soft Computing	
SAT	Elective-I (Data Science)	Elective-I (Data Science)	Elective-I (Data Science)				Advanced Computer Architecture	Advanced Computer Architecture	Advanced Computer Architecture

Subject Code	Subject Name	Faculty Name
	Advanced Computer Architecture	Dr.D.Thamaraiselvi
	Soft Computing	Dr.M.Gayathri
	Elective-I(Data Science)	Dr.M.Senthilkumaran
	Data Science Lab	Dr.M.Senthilkumaran

  
Dr. T. Lakshmibai  
CSE-Timetable Coordinator

  
Dr. P. Shanmugapriya  
Associate Head-CSE

  
Dr. M. Senthilkumaran  
HOD-CSE

  
Dr. M. Rathinakumar  
Dean - E & T